

# Diamond for high frequency devices, DNA sensors and superconductor

Department of Electronics and Photonic Systems,  
School of Science & Engineering, Waseda University,

Hiroshi Kawarada

1. **Doping technologies** realizing highly conductive diamond for new device application
2. **High frequency devices** based on interface properties
3. **DNA sensors for 1 base mismatch detection** using diamond surface by fluorescence and transistors

# Toward lighter element

III IV V

Two findings are an indication  
of coming a carbon century

B C N

1990 Carbon Nanotubes found in electron microscopy

1983 Diamond formed from gas phase

Al Si P

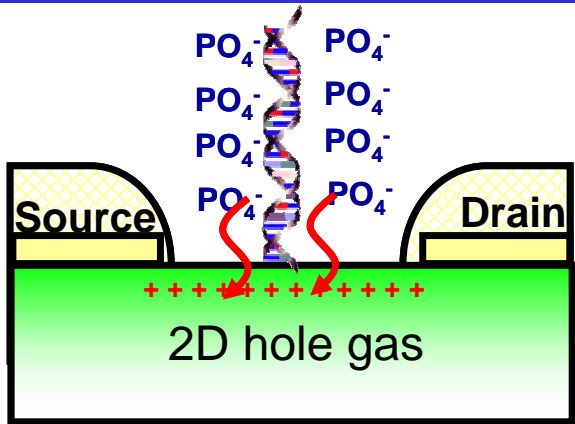
1960 Development of Transistors, ICs

Ga Ge As

1947 Invention of Transistors



# Carbon based nano and bioelectronics in Kawarada's group



## 1. Microwave devices

and their characteristics

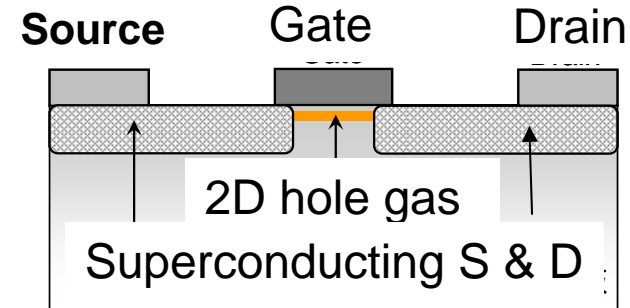
-Diamond MOSFET

$f_T$  45 GHz,  $f_{max}$  100GHz

IEEE Elect. Dev. Lett., **22**, 390 (2001)

**23**, 121(2002), **25**, 480(2004)

Appl. Phys. Lett. **88**, 112117 (2006)



## 3. Biosensor & bioelectronics

-Surface chemical modification

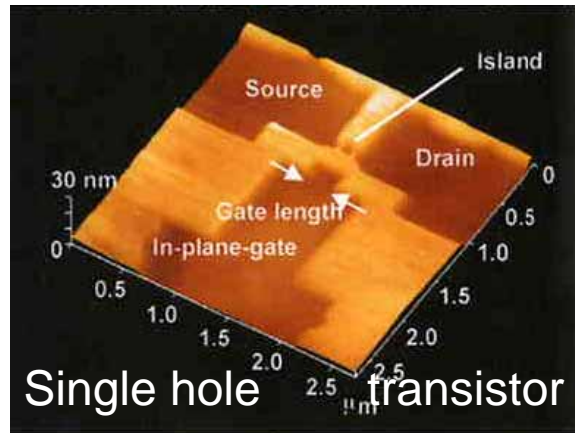
-Diamond solution gate FETs

for Biosensing DNA detection

Phys.Rev.E. **74**, 041919 (2006)

Langmuir, **22**, 11245 (2006)

Appl. Phys. Lett. **90**, 063901 (2007).



Appl. Phys. Lett., **81**, 2854 (2002).

## 2. Superconductivity

and transistor application

Highly B-doped  $>10^{21} \text{cm}^{-3}$

Diamond  $T_c \sim 10\text{K}$

Cryoelectronics

Y.Takano, H. Kawarada, et al.

Appl. Phys. Lett. **85**, 2851(2004)

T. Yokoya, H. Kawarada, et al.

Nature, **438**, 647-650 (2005)

## 4. Carbon nanotubes

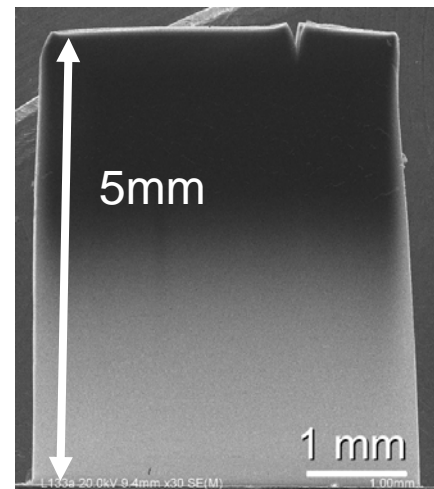
Densely packed & vertically oriented  
single or double wall carbon nanotube  
for interconnection and super capacitor

J. Phys. Chem. B, **109**, 19556 (2005)

Carbon, **44**, 2009 (2006)

J. Phys. Chem. B, **111**, 1907 (2007)

State of art  
plasma  
deposition

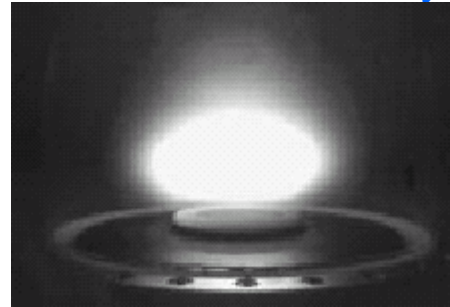


# Poly crystalline diamond substrate

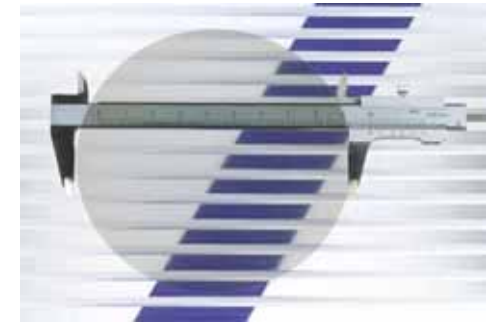
Courtesy from Fraunhofer Institute, Freiburg



**Ellipsoidal Microwave Plasma Reactor**



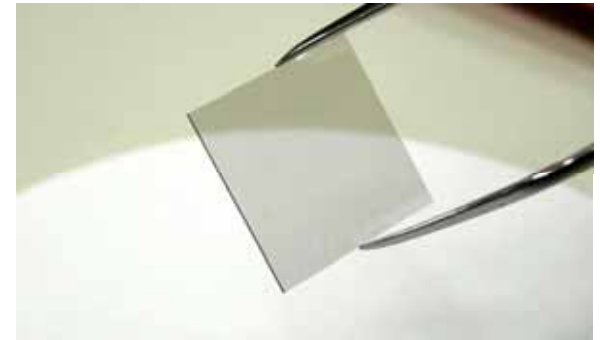
**Plasma cloud  
in the vicinity of substrate**



**CVD diamond wafer  
with diameter of 10 cm**

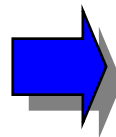


**Various kind of  
CVD diamond**

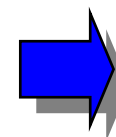


**Diamond chip ( 5 by 5 mm )**

**Plasma Decomposition  
of CH<sub>4</sub> and H<sub>2</sub>**



**CH<sub>3</sub> and H  
radicals**



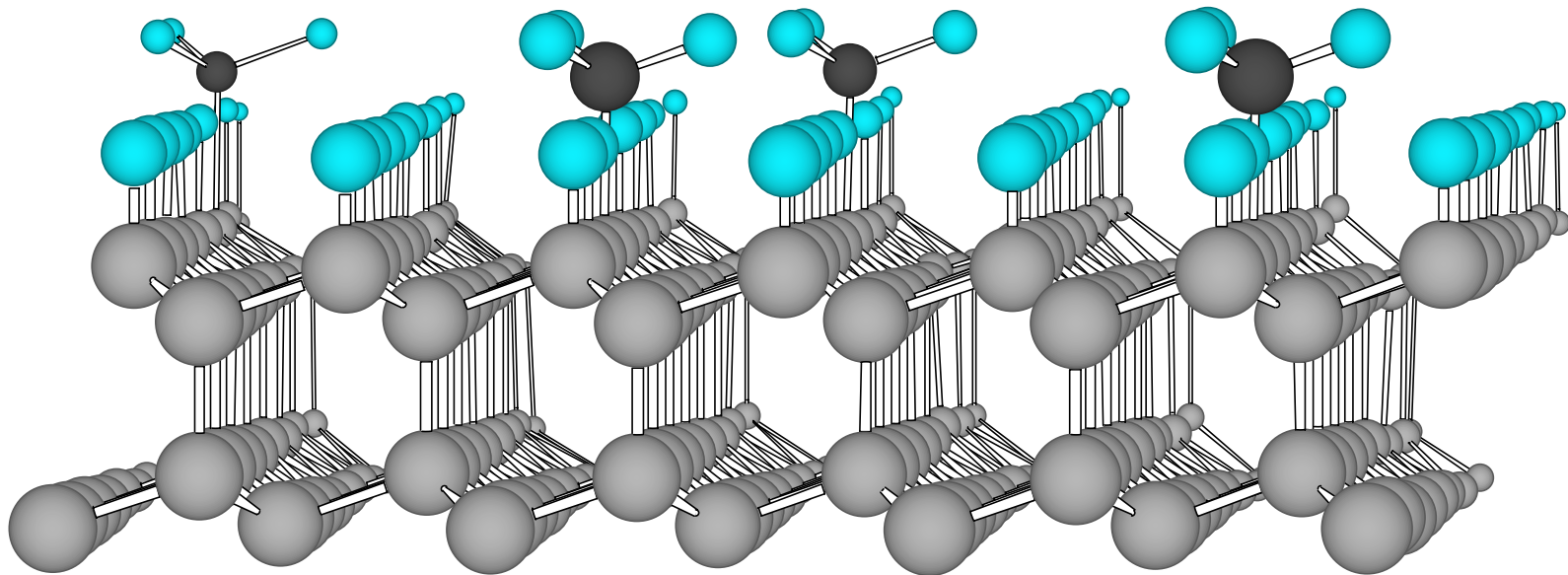
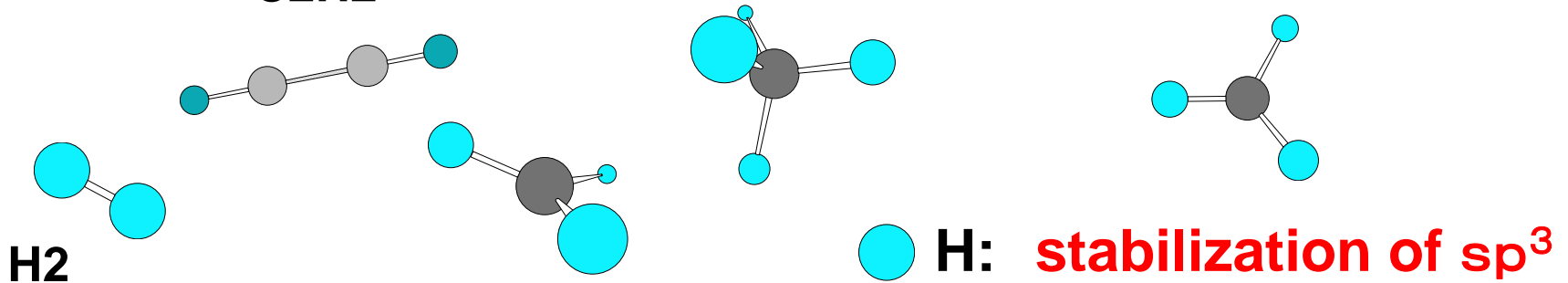
**Diamond or  
Carbon  
Nanotube**

# Diamond surface during growth

CH<sub>4</sub>:methane

CH<sub>3</sub>:methyl radicals

C<sub>2</sub>H<sub>2</sub>



# Outline for Subject 1 and 2

- **Semi- and Superconductive Diamonds**
- **2 D Hole Gas** and Surface Channel FETs  
with H-terminated Surface
- Gate oxide  $\Rightarrow$  **Al<sub>2</sub>O<sub>3</sub>**, higher gate-voltage-swing
- **Mobility** Improvement
- **Carrier Transport: Velocity and Mobility**  
Compared with SiC or GaN FETs
- **Summary**

# Diamond as wide gap semiconductor

	Si	GaAs	6H SiC	GaN	Diamond
Bandgap $E_G$ [eV]	1.1	1.43	3.10	3.45	<b>5.45</b>
Saturated drift velocity $v_S$ [ $10^7$ cm/s]	1.0	1.0	2.0	2.2	<b>1.0 (hole)</b>
Carrier mobility $\mu$ [ $\text{cm}^2/\text{V}\cdot\text{s}$ ]	1500	8500	1140	1250	3800(hole)
Breakdown field $E_B$ [MV/cm]	0.3	0.4	3	2	<b>~10</b>
Dielectric constant $\epsilon_r$	11.8	12.5	9.6/10	9	5.5
Thermal conductivity $\lambda$ [W/cm·K]	1.5	0.5	4.9	1.3	<b>22.0</b>
Johnson's figure of merit [ $10^{23} \Omega\cdot\text{W}/\text{s}^2$ ]	2.3	9.1	910	1080	2530(hole)
Keyes' figure of merit [ $10^7$ W/K·s]	6.7	2.0	35	10	145(hole)
Baliga's figure of merit [Si = 1]	1	48	620	24	43938 (hole)

**Johnson's figure of merit**  
Frequency & power products  
of transistors

$$JFM = \left( \frac{E_B \cdot v_S}{2\pi} \right)^2$$

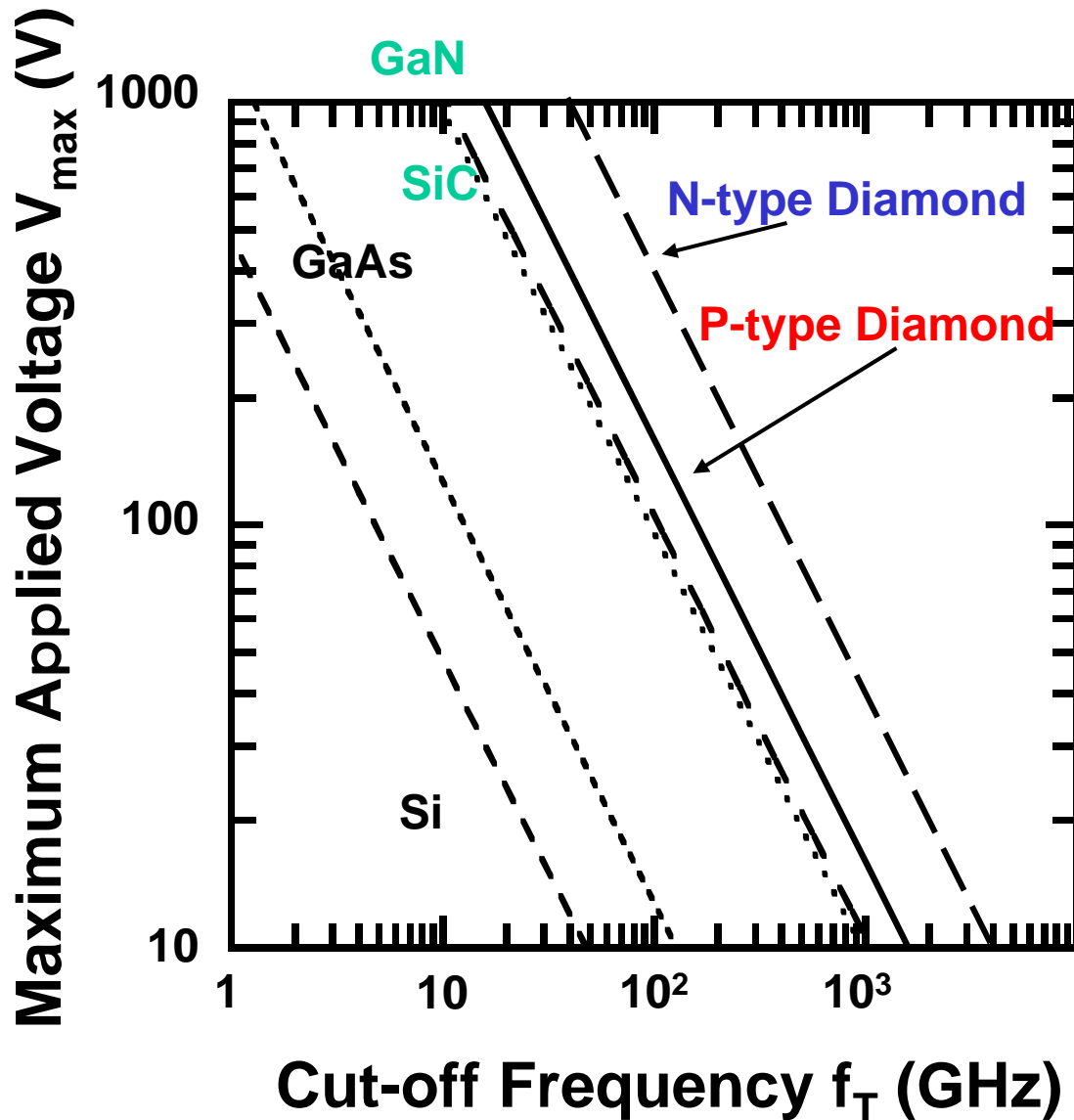
**Keyes' figure of merit**  
Thermal limitation on  
high-frequency performance

$$KFM = \lambda \left( \frac{c \cdot v_S}{2\pi \cdot \epsilon_r} \right)^{\frac{1}{2}}$$

**Baliga's figure of merit**  
Loss in high-power &  
high-frequency operation

$$BFM = \epsilon \cdot \mu \cdot E_B^3$$

# Diamond : high power & high frequency device



## Cut-off Frequency

$$f_T = \frac{v_s}{2\pi L_g}$$

vs: Saturated Velocity  
P-type Diamond  $10^7$  cm/s

## Maximum Voltage

$$V_{\max} = E_b L_g$$

$E_b$ : Breakdown Field  
Diamond  $10^7$  V/cm

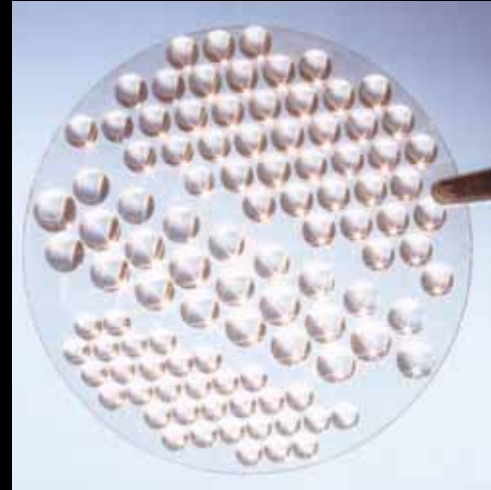
$$f_T V_{\max} = \frac{v_s E_b}{2\pi} = \text{const.}$$



# Semiconducting Diamonds



**Blue diamond**  
**Hope Diamond**  
(Smithsonian Museum)



**CVD diamond**  
Poly ~  $\phi$ 150mm  
Single ~  $\phi$   
Hetero ~  $\phi$

Hole activation energy: 0.37 eV

Carriers at RT not expected.

**Boron doped diamond (p-type)**

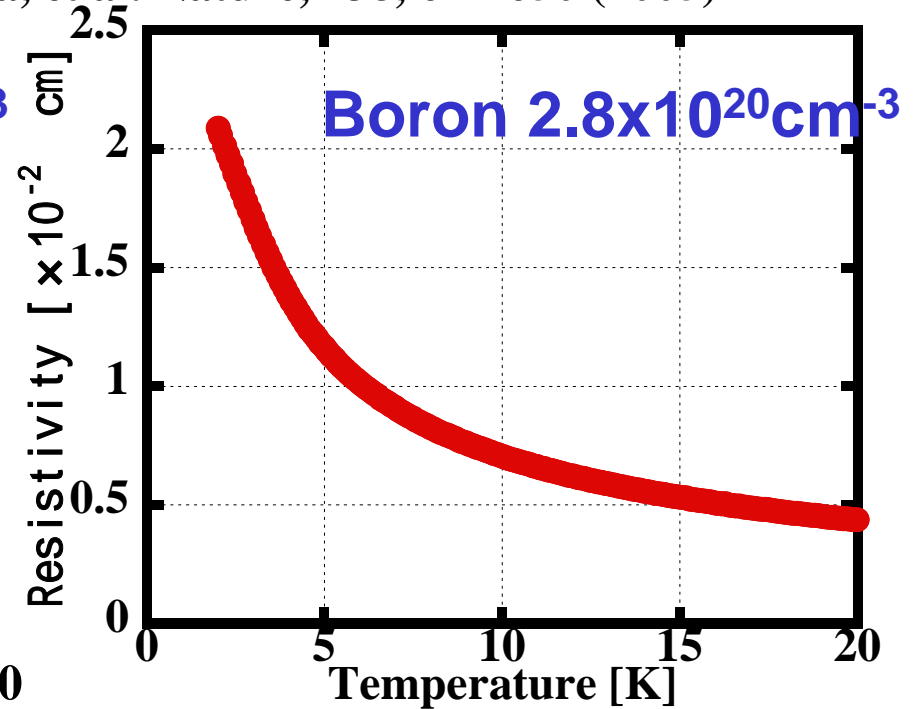
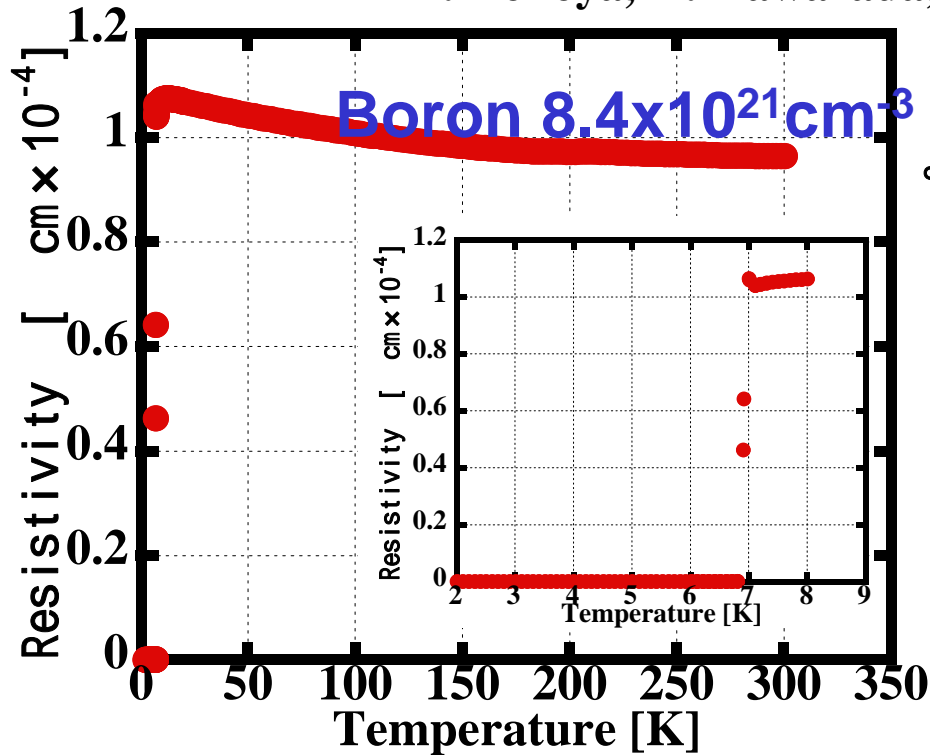
**Hydrogen-termination**  
**(p-type)**

$10^{13} \text{ cm}^{-2}$  surface carrier density  
at RT

**CVD diamond (insulating)**

# Superconducting diamond by boron doping

T. Yokoya, H. Kawarada, et al. Nature, 438, 647-650 (2005)

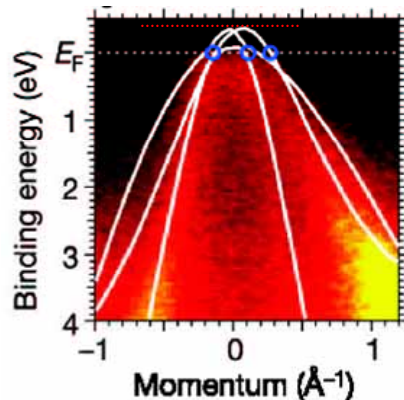


## Superconductivity

$T_c = 7.4 \text{K}$

$N_b = 8.37 \times 10^{21} [\text{cm}^{-3}]$

$N_c = 1.23 \times 10^{22} [\text{cm}^{-3}]$

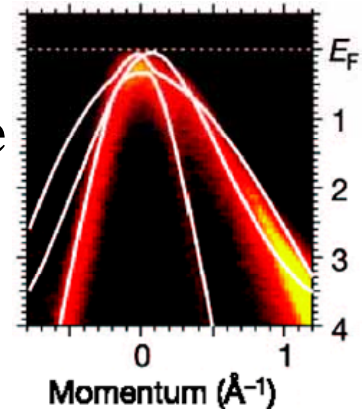


$0.40 \pm 0.2 \text{ eV}$

## Variable Range Hopping

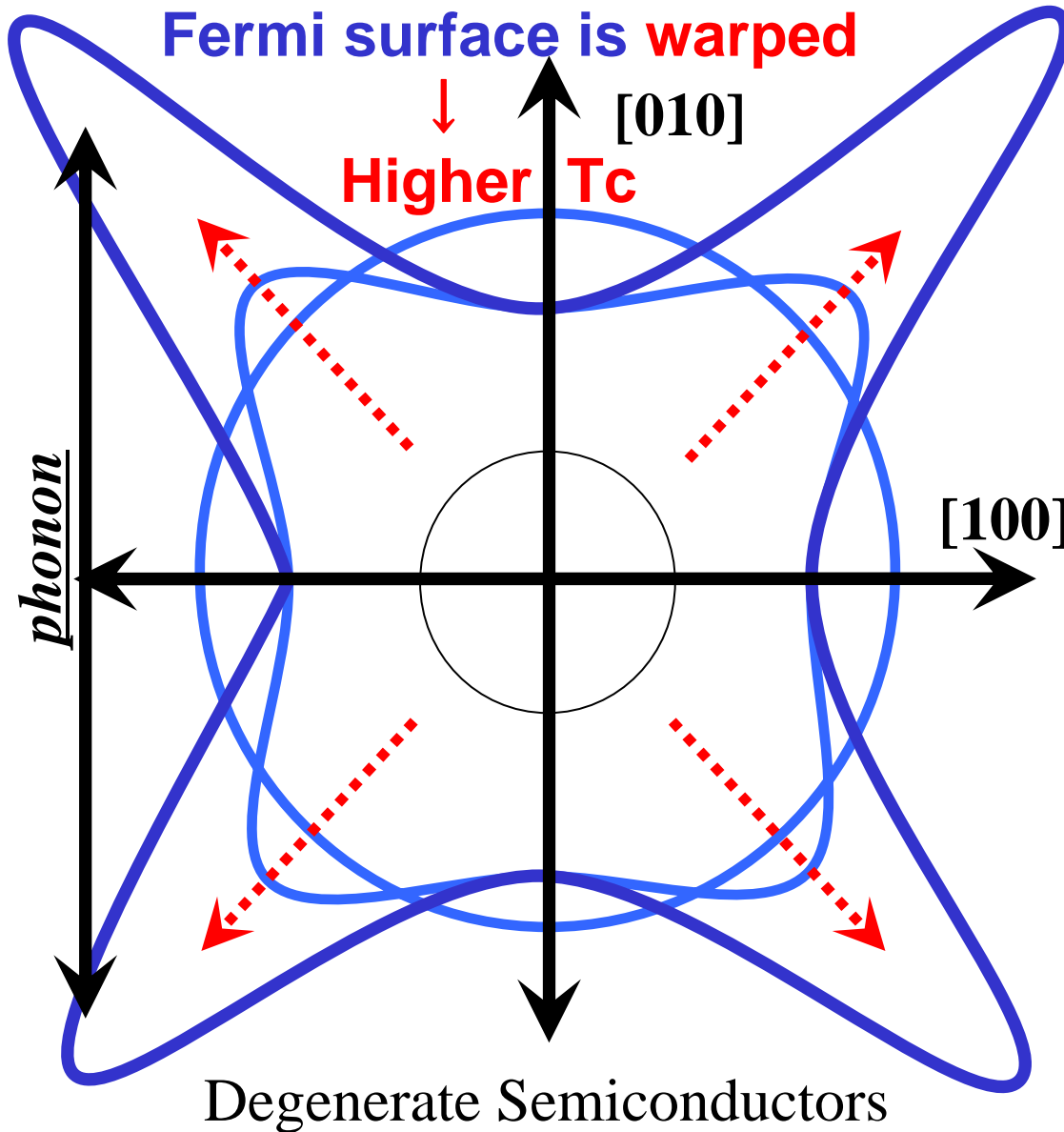
$N_b = 2.80 \times 10^{20} [\text{cm}^{-3}]$

$N_c = 2.92 \times 10^{20} [\text{cm}^{-3}]$



# Warped Fermi Surface → Higher $T_c$

As Hall factor  $r_H$  decreases  
Fermi surface is **warped**



Hall factor  $r_H$  evaluation of the shape of Fermi surface

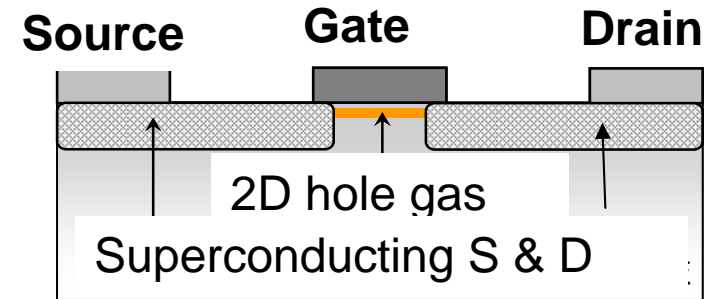
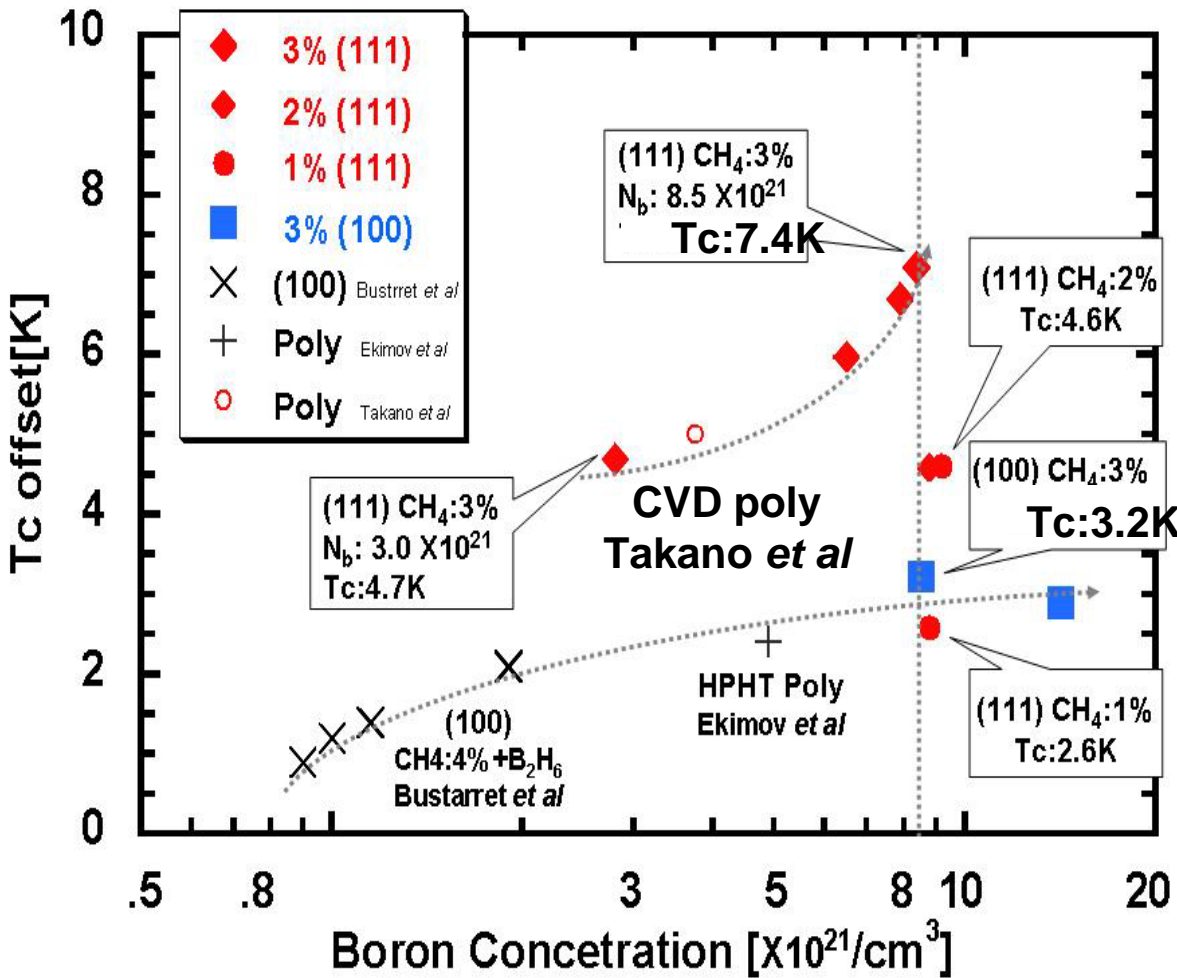
Carrier (hole) concentration is observed to be higher than acceptor (boron) concentration if we assume the Hall factor  $r_H$  is 1.

$$p \text{ (hole)} = N_B \text{ (boron)} / r_H$$

Estimated  $r_H$  is less than 1 and depends on  $T_c$ .

The lower  $r_H$ , the higher  $T_c$  in boron-doped diamond

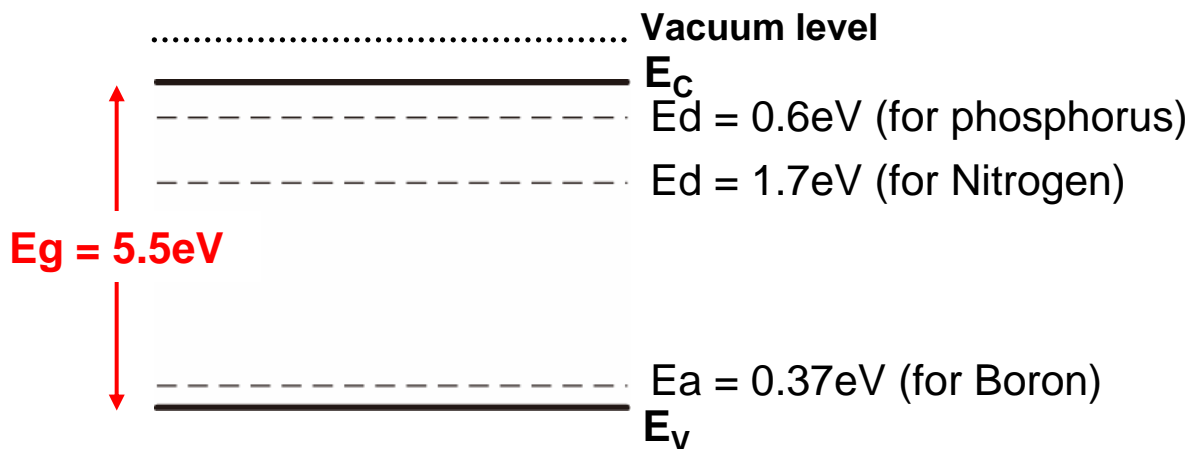
# Superconductivity from $T_c$ offset by boron doped diamond and a new cryodevice



New type of cryodevice based on **superconductor** and **semiconductor** on diamond

# 2D hole gas transistors using diamond

- ◆ Device using **boron-doped bulk diamond** (MOS, MESFET, Bipolar)

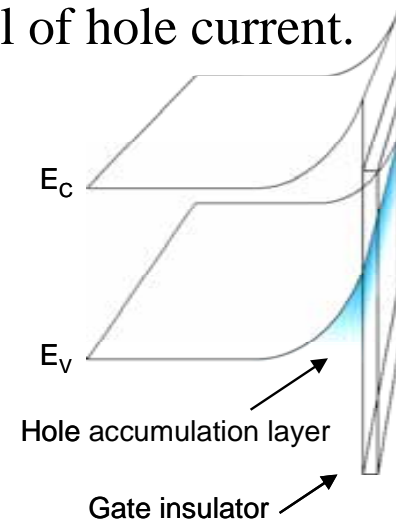
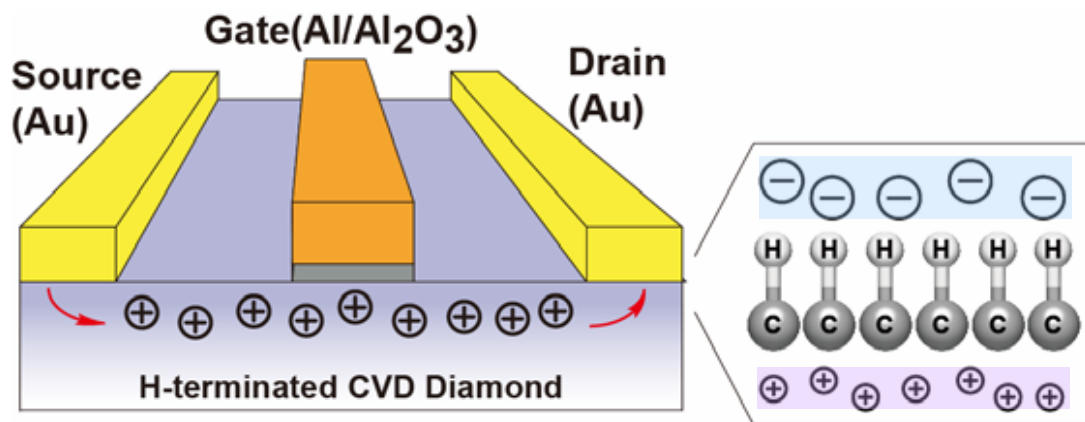


High activation energies

Most of impurity atoms can be hardly ionized @R.T.

- ◆ Device using 2D hole gas (accumulation) (MOSFET, MESFET)

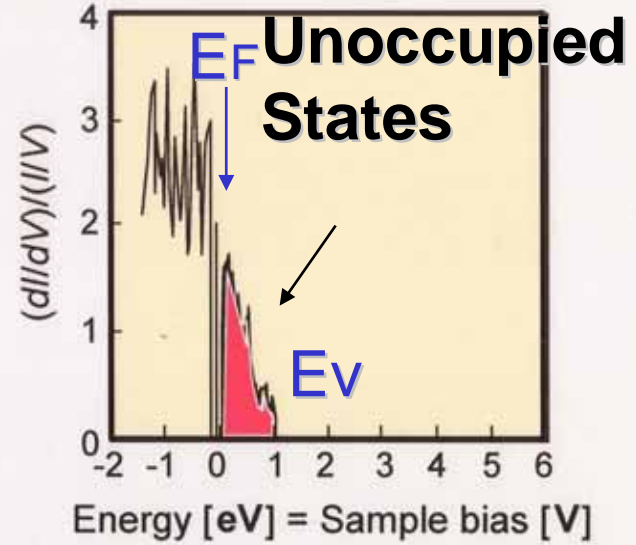
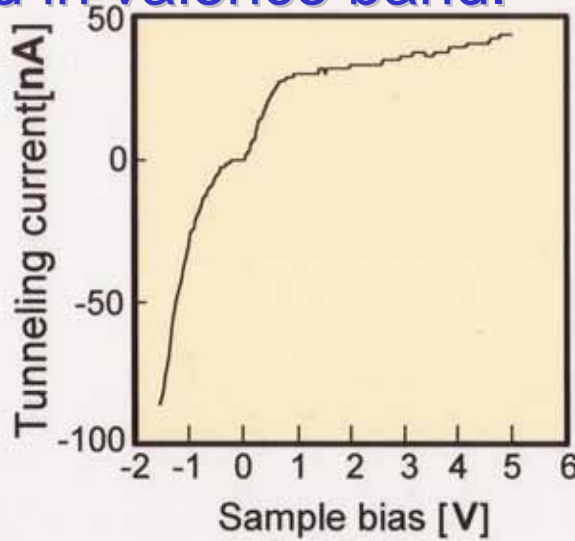
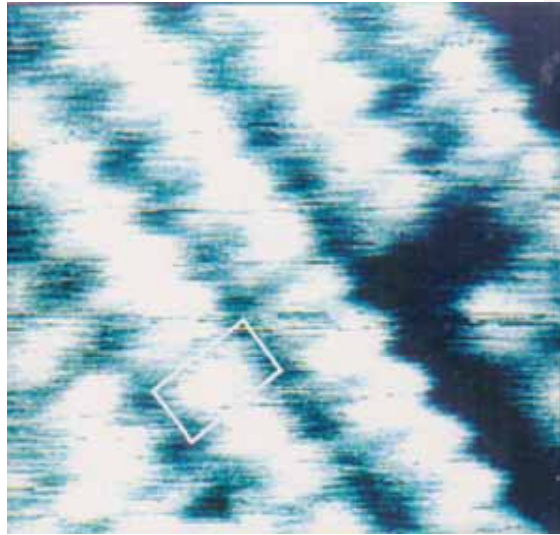
In this device, the diamond substrate is **un-doped**, and **the hole accumulation layer on hydrogen-terminated surface** is used as a channel of hole current.



Most of diamond RF transistors have been fabricated using hole accumulation layer.

# STS Spectrum of H-terminated diamond (001)

Due to high density of surface states,  
Fermi level is located in valence band.



H.Kawarada et al. **1nm**

Phy. Rev. B 52

(1995) 11351.

Tunneling current vs.  
sample bias characteristics

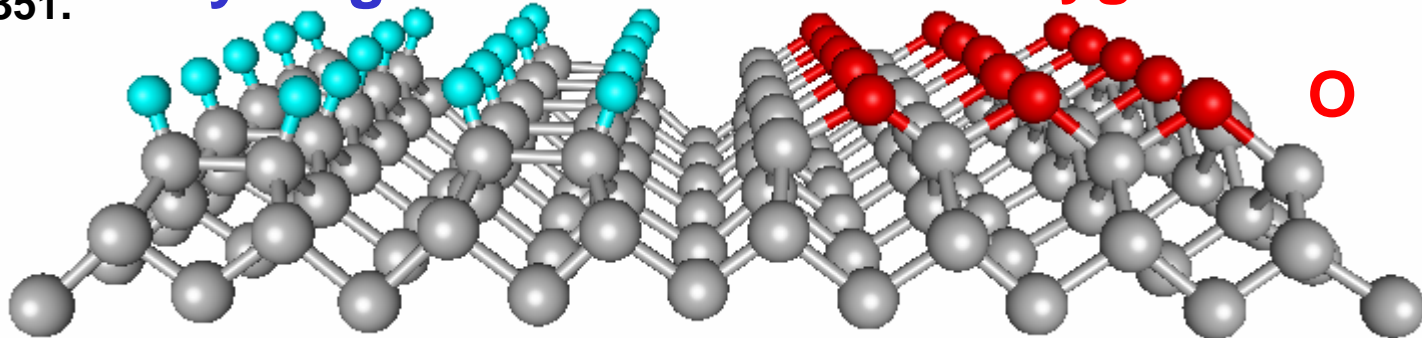
$(dI/dV)/(I/V)$  vs. sample bias.

**Hydrogen termination**

**Oxygen termination**

**H**

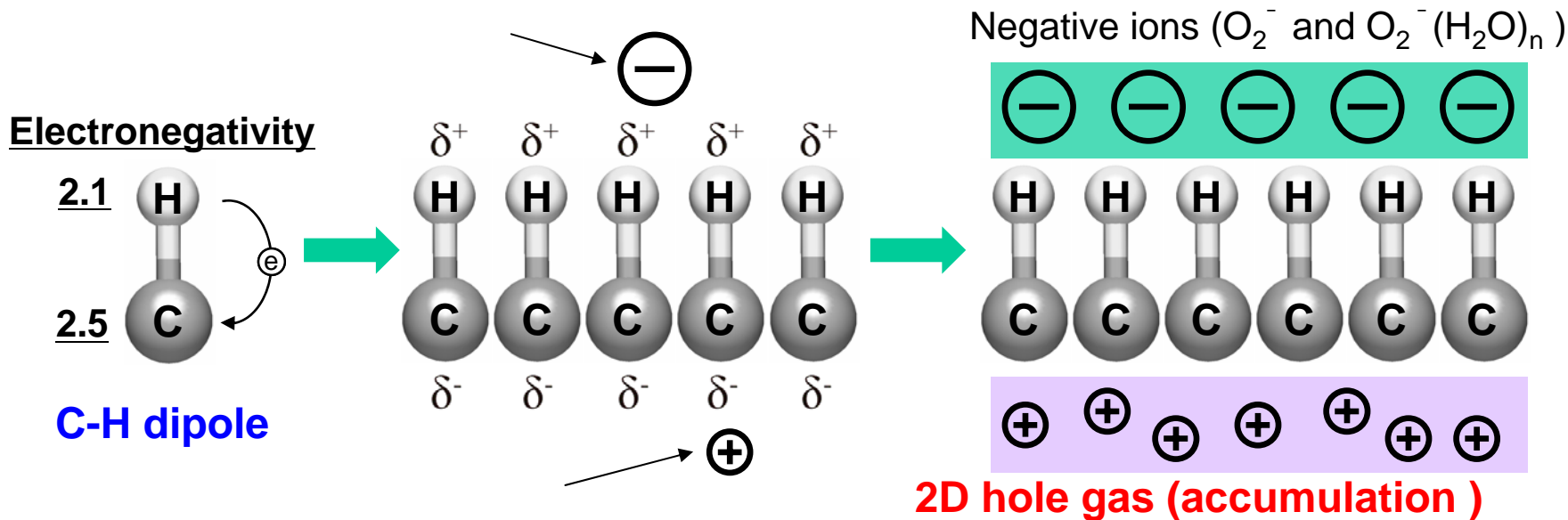
**O**



**P-type (Carrier density  $\sim 10^{13} \text{ cm}^{-2}$ )**

**Insulating**

# 2D hole gas layer on hydrogen-terminated surface



Hydrogen-terminated diamond surface has **positive charge by C-H dipole**.

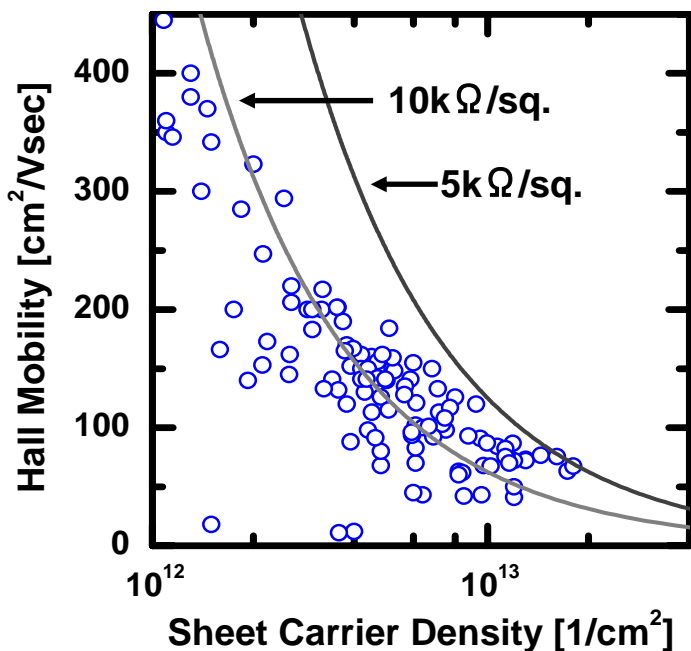
When the surface is exposed to air, negatively charged ions are captured by the positively charged surface.

The negatively charged ions induce upward band-bending on diamond surface.

Finally, holes are accumulated on the surface to form

**2 dimensional hole gas**

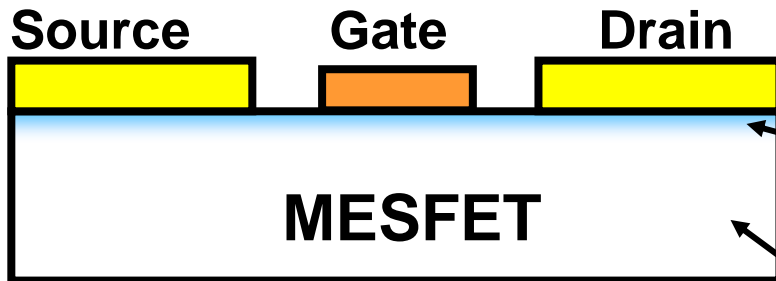
**Sheet carrier density over  $10^{12} \text{ cm}^{-2}$**  can be easily obtained.



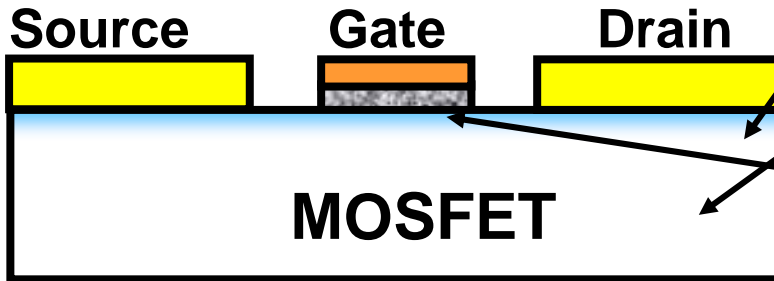
# Diamond Field Effect Transistors (FETs)

## MESFET and MOSFET

**Cu as Schottky gate electrode**



**>20nm  $\text{Al}_2\text{O}_3$  as gate insulator**



IEEE Elect. Dev. Lett, **22**, 390 (2001)  
**23**, 121(2002), **25**, 480(2004)

Shallow carrier profile < 10 nm  
High carrier density >  $10^{13} \text{ cm}^{-2}$   
inversion layer of Si  $10^{11} \sim 10^{12} \text{ cm}^{-2}$

Highest thermal conductivity  
22 W/cm·K

Low density of pinning states  
<  $10^{11} \text{ cm}^{-2}$

### Advantages of MESFET

- Easy to fabrication
- High transconductance

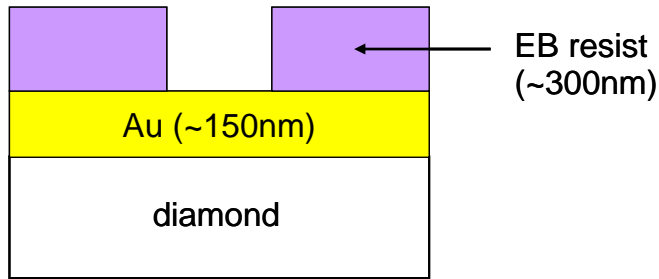
### Advantages of MOSFET

- Low gate leakage current
- Resistant for gate enhancement
- Stabilization of surface conductive layer

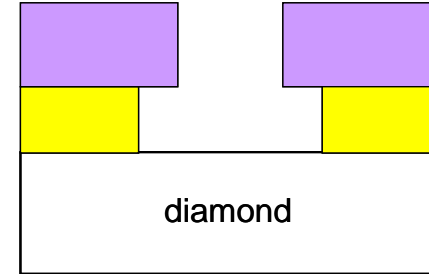


# How to make diamond field effect transistors

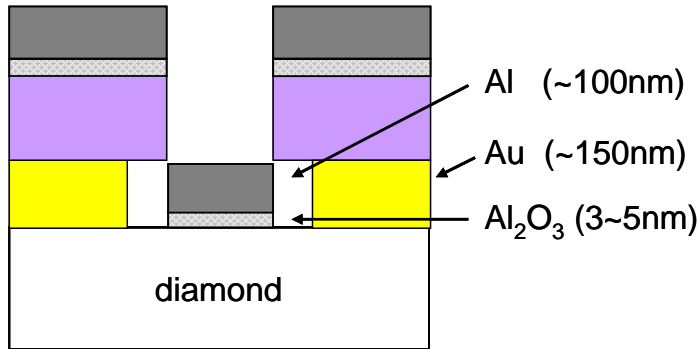
## Self-alignment technique



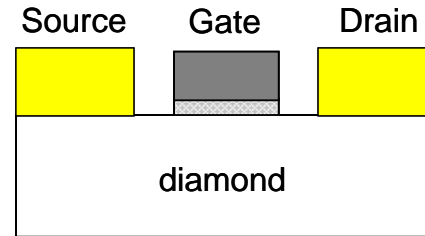
(a) EB lithography



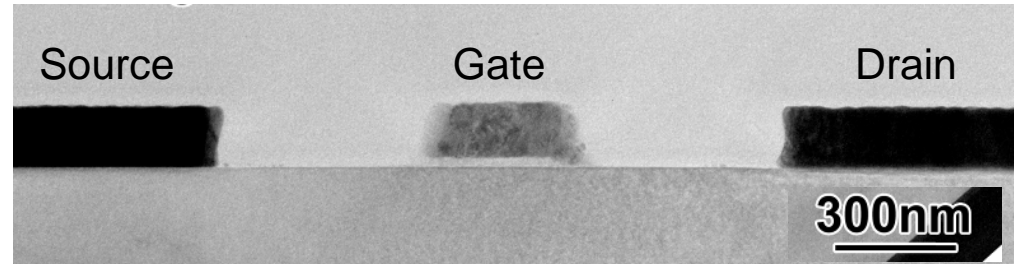
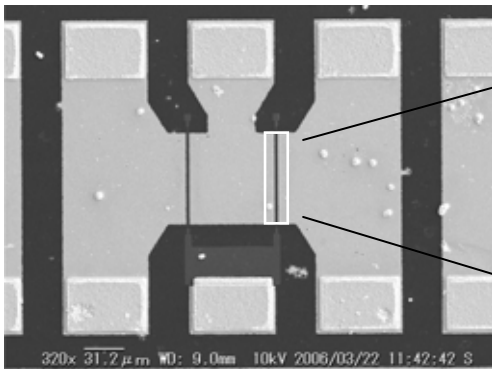
(b) Au etching



(c) Al & Al<sub>2</sub>O<sub>3</sub> deposition



(d) Lift-off



# Development of Diamond RF Devices

## RF Diamond MESFETs

### **2001** First small signal RF measurement

Waseda Univ., H. Taniuchi *et al.*, IEEE  
Electron Device Letter (EDL) 22 (2001) 390

---

### **2002** First RF power measurement

Ulm Univ., A. Aleksov *et al.* Device Research  
Conference proceedings (2002) 181

---

### **2004** First Noise Figure measurement

$f_T$  : 24.6GHz and  $f_{max}$  : 80GHz

Ulm Univ. and NTT BRL, M. Kubovic *et al.*,  
DRM 13 (2004) 802

---

### **2005** 2W/mm output power @ 1GHz

NTT BRL, M. Kasu *et al.*, Electronics  
Letter, 41 (2005) 22

---

### **2006** $f_T$ : 45GHz and $f_{max}$ : 120GHz

NTT BRL, M. Kasu *et al.*, IEEE EDL, 27  
(2006) 570

## RF Diamond MOSFETs

### **2002** First small signal RF measurement

Waseda Univ., H. Umezawa *et al.* IEEE  
EDL 23 (2002) 121

---

### **2004** $f_T$ : 23GHz

Waseda Univ., H. Matsudaira *et al.*, IEEE  
EDL 25 (2004) 408

---

### **2006** Highest $f_T$ of 30GHz for single crystal diamond FETs

Waseda Univ., K. Hiramata *et al.*, IEEE  
International Symposium on Power  
Semiconductor Devices and ICs (ISPSD)  
proceedings (2006) 49

---

### **2007** Highest $f_T$ of 45GHz for diamond FETs

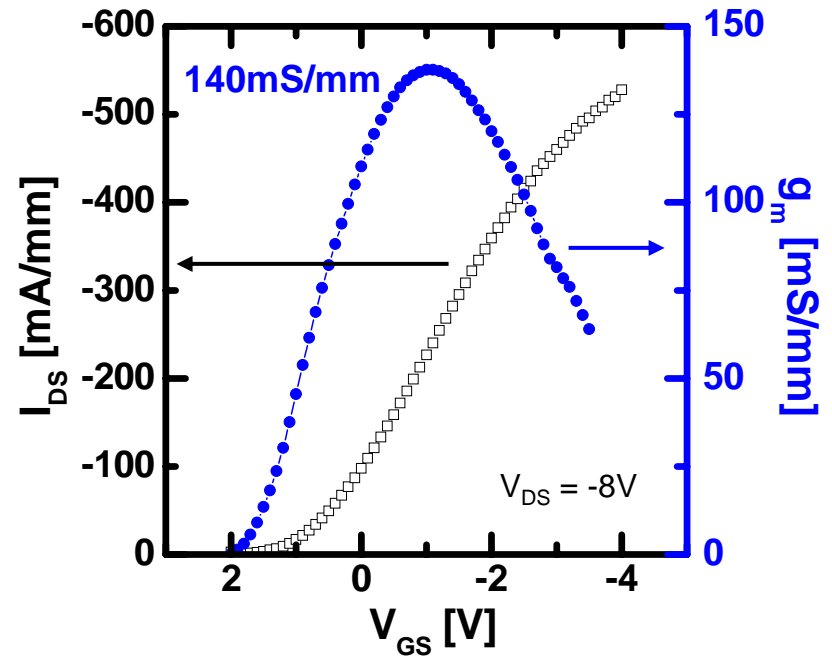
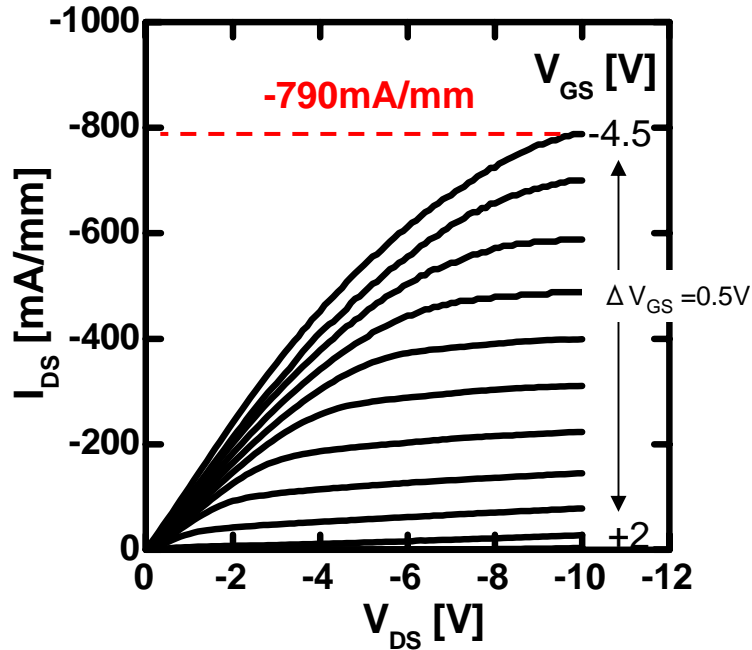
Highest  $I_{DS}$  of -790mA/mm

Waseda Univ., K. Hiramata *et al.*, IEEE ISPSD  
proceedings (2007) 269

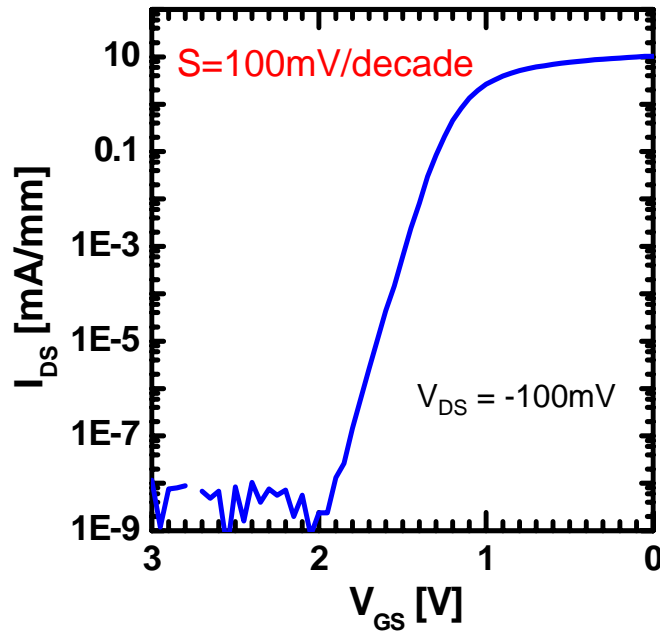
Waseda Univ. fabricated RF diamond MES and MISFETs for the first time, and has improved MISFETs.

Ulm Univ. and NTT BRL have improved MESFETs and demonstrated power characteristics for the first time.

# 0.25 $\mu\text{m}$ -gate-length MOSFET



Normally-on operation



Gate length:  $0.25\mu\text{m}$

Gate width:  $25\mu\text{m}$

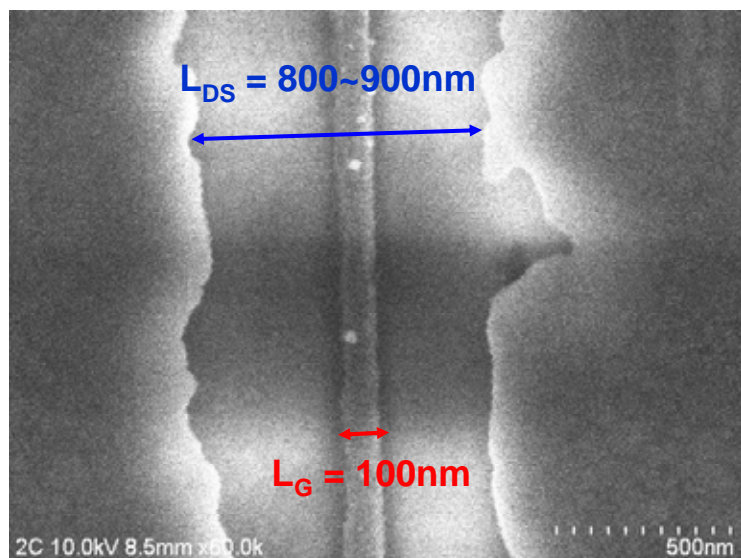
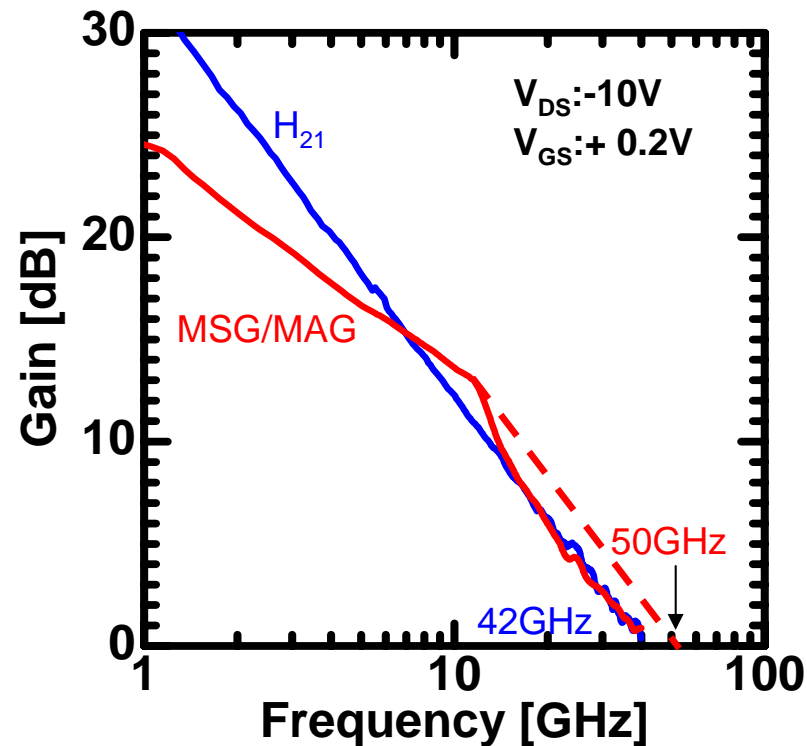
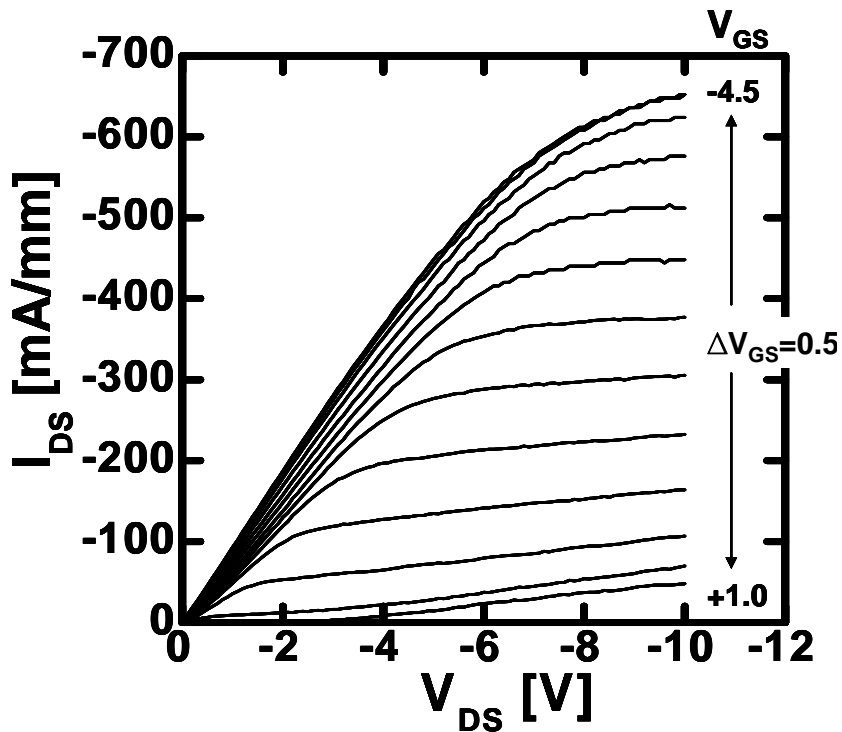
**Maximum drain current: -790 mA/mm**

**Maximum  $g_m$ : 140 mS/mm**

**Subthreshold factor: 100 mV/decade**

The  $I_{DS}$  of **790 mA/mm** is the highest value for diamond FETs.

# 0.1 $\mu\text{m}$ -gate-length MOSFET



Gate length: 0.1  $\mu\text{m}$

Gate width: 25  $\mu\text{m}$

**Maximum drain current: -650mA/mm**

**$f_{\text{T}}$ : 42GHz**

**Mason's U: 50GHz**

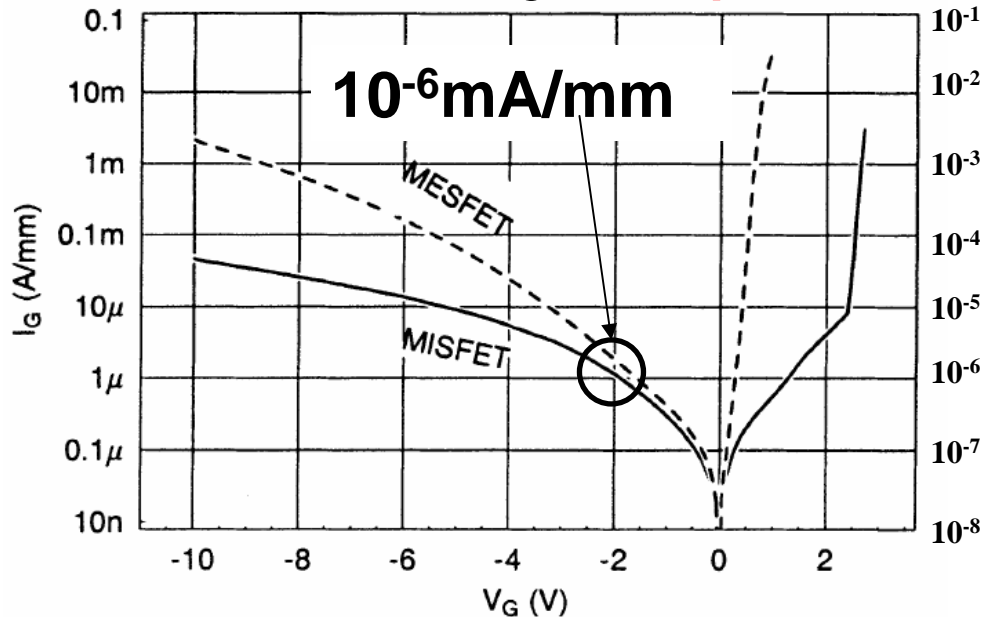
# Al<sub>2</sub>O<sub>3</sub> Gate Insulator

## Gate Leakage Characteristic

GaN MISFET

SiN thickness : 4nm

Gate length : 0.7μm

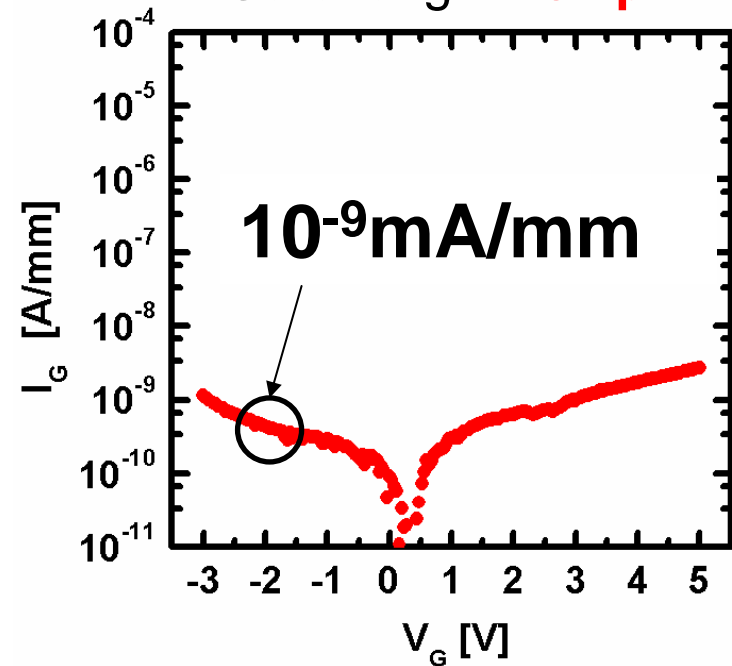


A.Chini et al. (IEEE EDL Vol.25 No.2 2004)

Diamond MOSFET

Al<sub>2</sub>O<sub>3</sub> thickness : 3nm

Gate length : 0.4μm



Using Al<sub>2</sub>O<sub>3</sub> as gate insulator, gate leakage of Diamond MOSFET was **three digit lower** than SiN gate insulator GaN MISFET

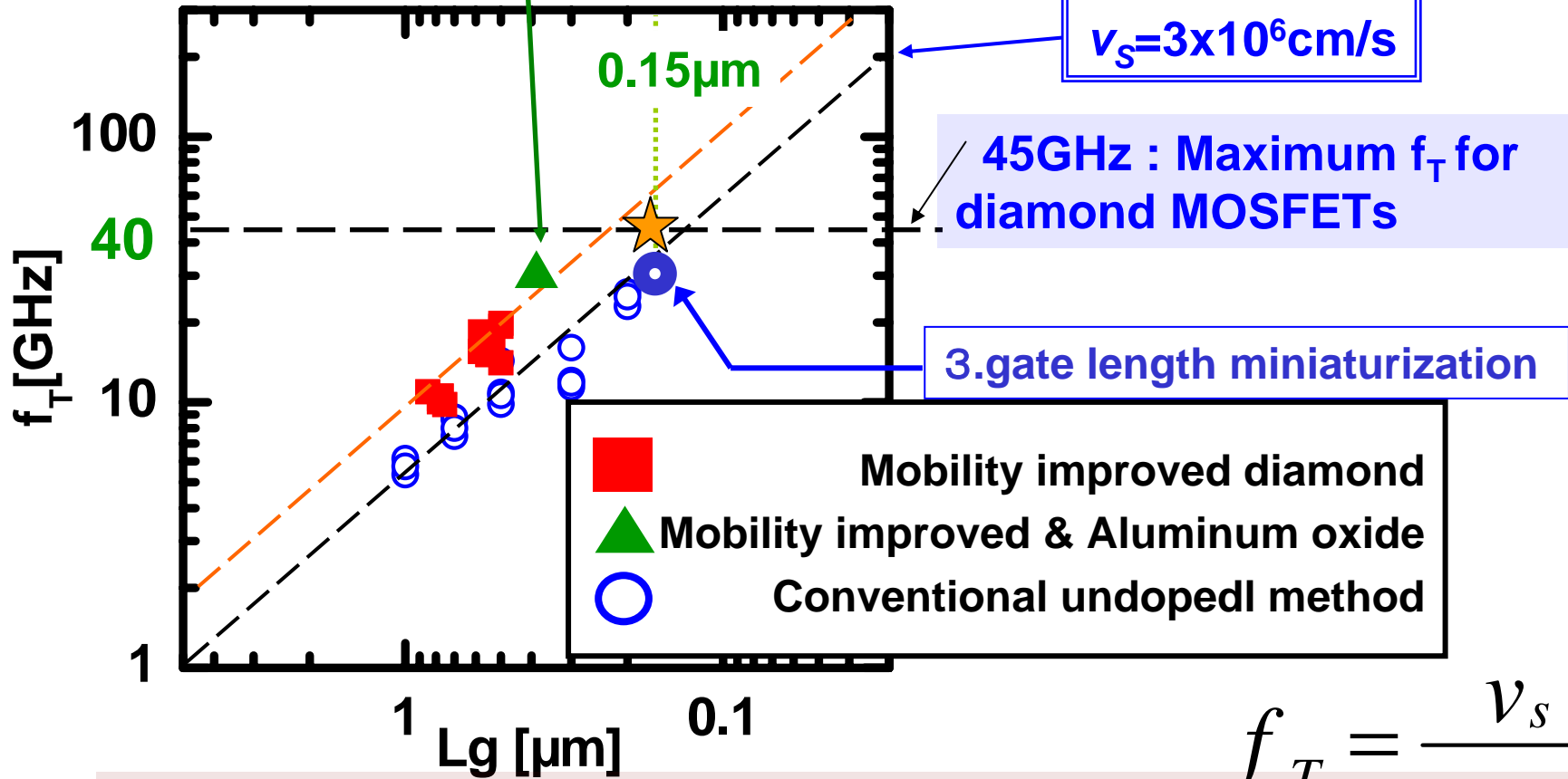
# Cut off frequency and velocity

IEEE Elect. Dev. Lett, **22**, 390 (2001)  
**23**, 121(2002), **25**, 480(2004)

- 1. high channel mobility &
- 2. Al<sub>2</sub>O<sub>3</sub> gate insulator

$$v_s = 6 \times 10^6 \text{ cm/s}$$

$$v_s = 3 \times 10^6 \text{ cm/s}$$



45GHz : Maximum  $f_T$  for diamond MOSFETs

3. gate length miniaturization

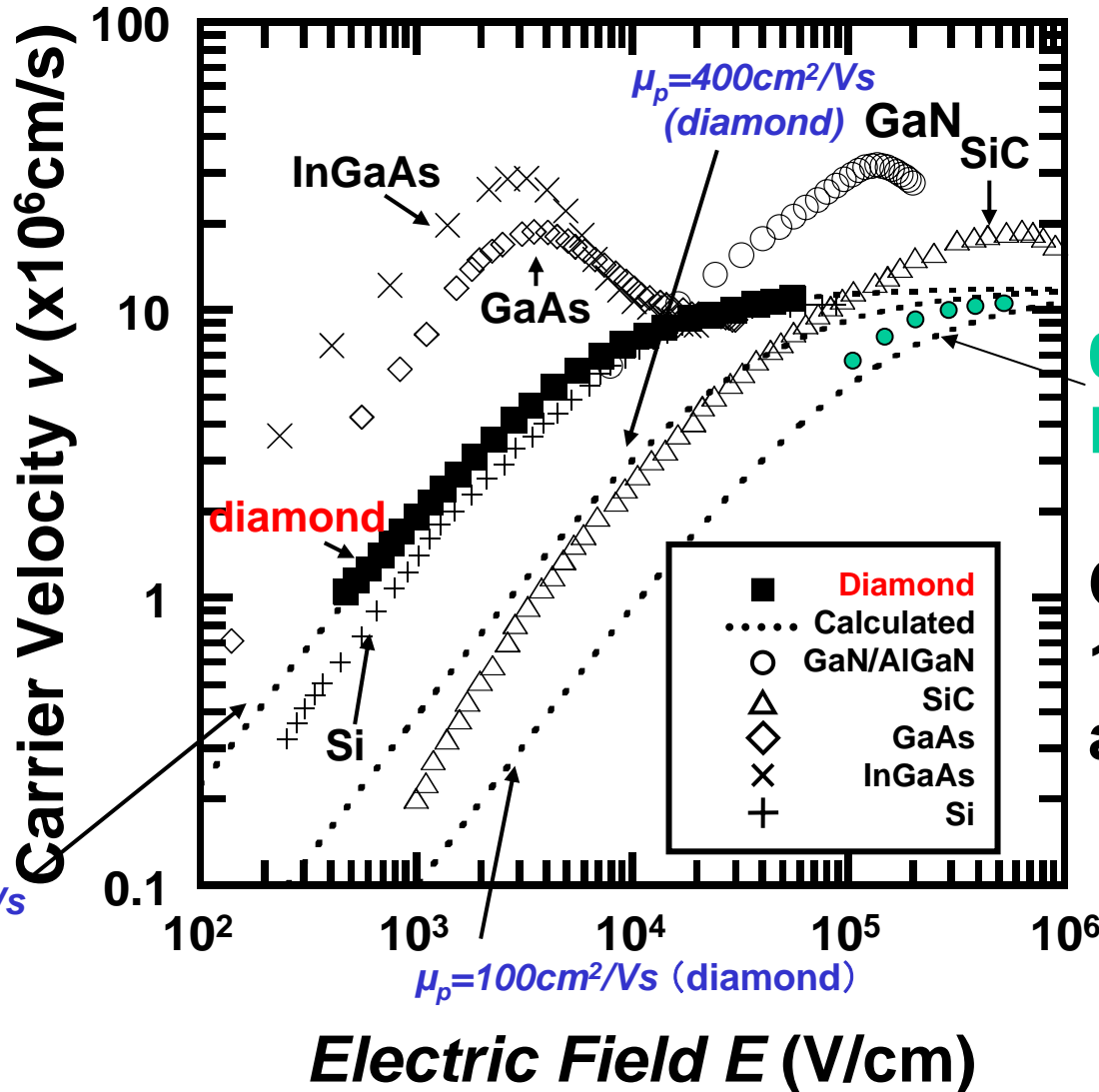
- Mobility improved diamond
- ▲ Mobility improved & Aluminum oxide
- Conventional undoped method

$$f_T = \frac{v_s}{2\pi L_g}$$

★ High channel mobility, Al<sub>2</sub>O<sub>3</sub> gate insulator, gate length miniaturization →  $f_T = 42\text{GHz}$

# Diamond transistors operated at high Electric Field

## Carrier Velocity dependence on Electric Field



0.1-0.2  $\mu\text{m}$  Gate  
Diamond FET

Channel Mobility  
100-200  $\text{cm}^2/\text{Vs}$   
at RF Operation

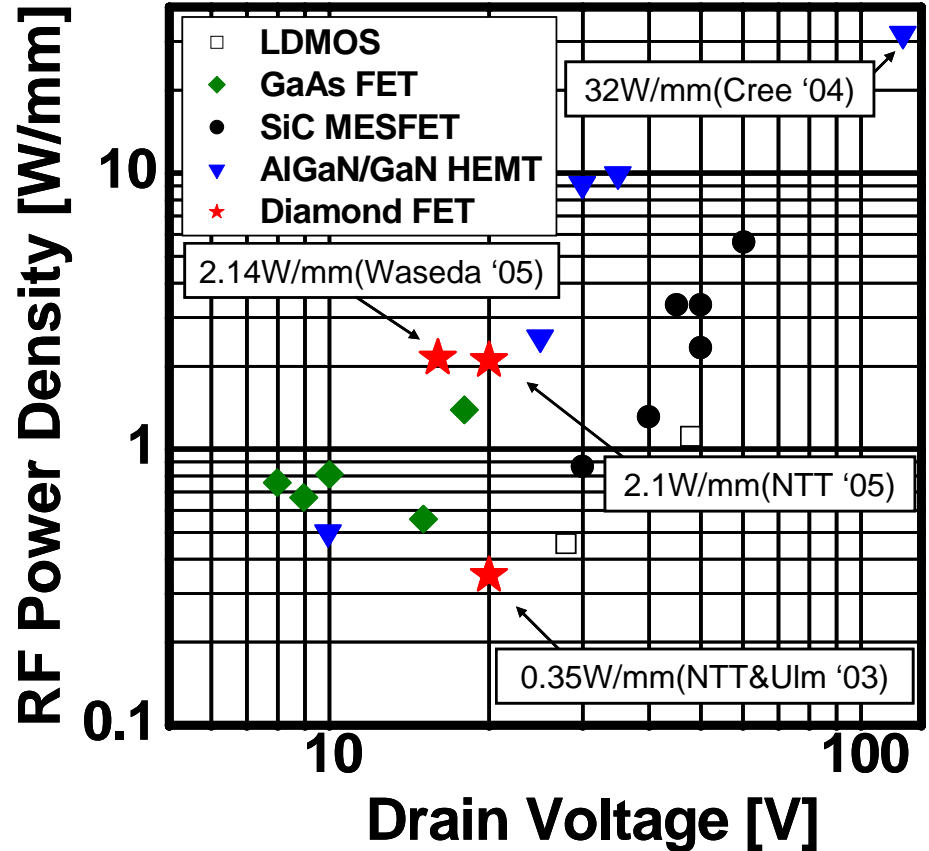
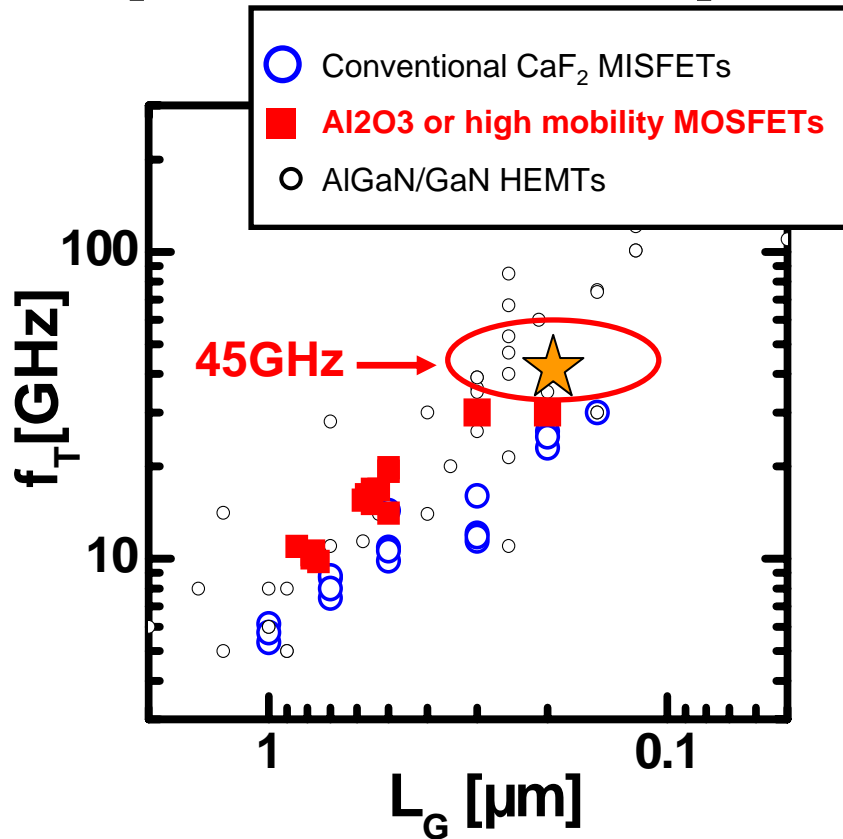
$\mu_p = 2200 \text{ cm}^2/\text{Vs}$   
(diamond)

$\mu_p = 100 \text{ cm}^2/\text{Vs}$  (diamond)

$\mu_p = 400 \text{ cm}^2/\text{Vs}$   
(diamond)

# RF performance comparable with AlGaN/GaN

Compared with Si, GaAs, SiC, AlGaN/GaN, diamond FETs shows comparable or better device performance.



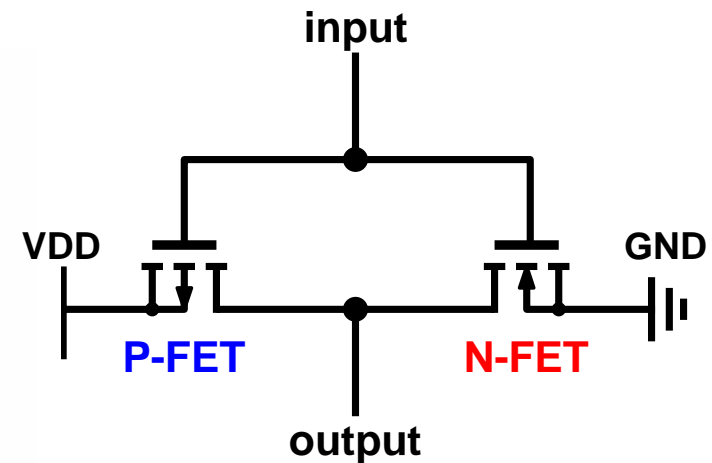
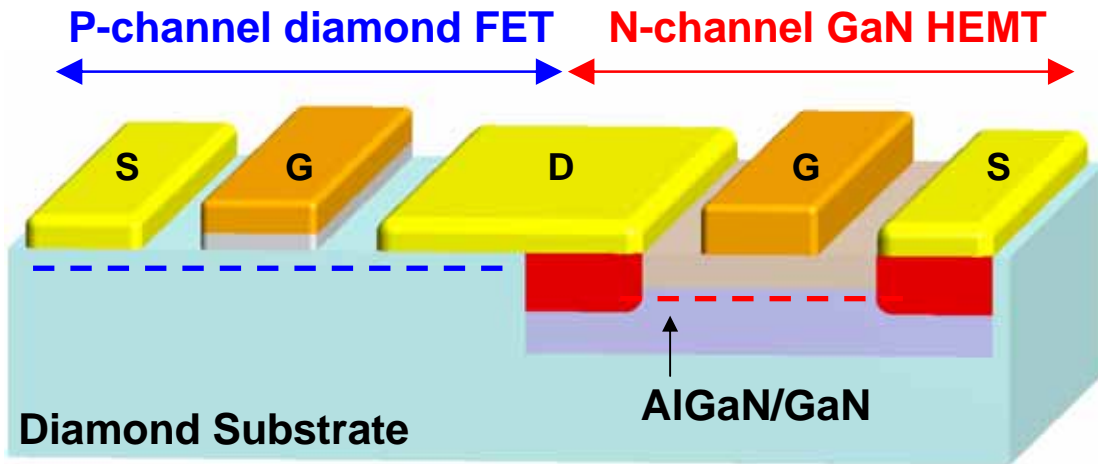
The  $f_T$  of 45 GHz is higher than that of SiC MESFETs

The RF power densities of diamond FETs have exceeded those of Si LDMOSFET and GaAs FETs.



# Diamond coexists with AlGaIn/GaN

DC and RF performance of p-channel diamond FETs is steady improving and approaching GaN HEMTs.



**high power RF complementary device.**

High diamond thermal conductivity can suppress the self-heating of high power devices. Complementary devices have a wide range of application.

- mixer
- voltage-controlled oscillator
- high efficiency power amplifier (D,E class)

# Summary I

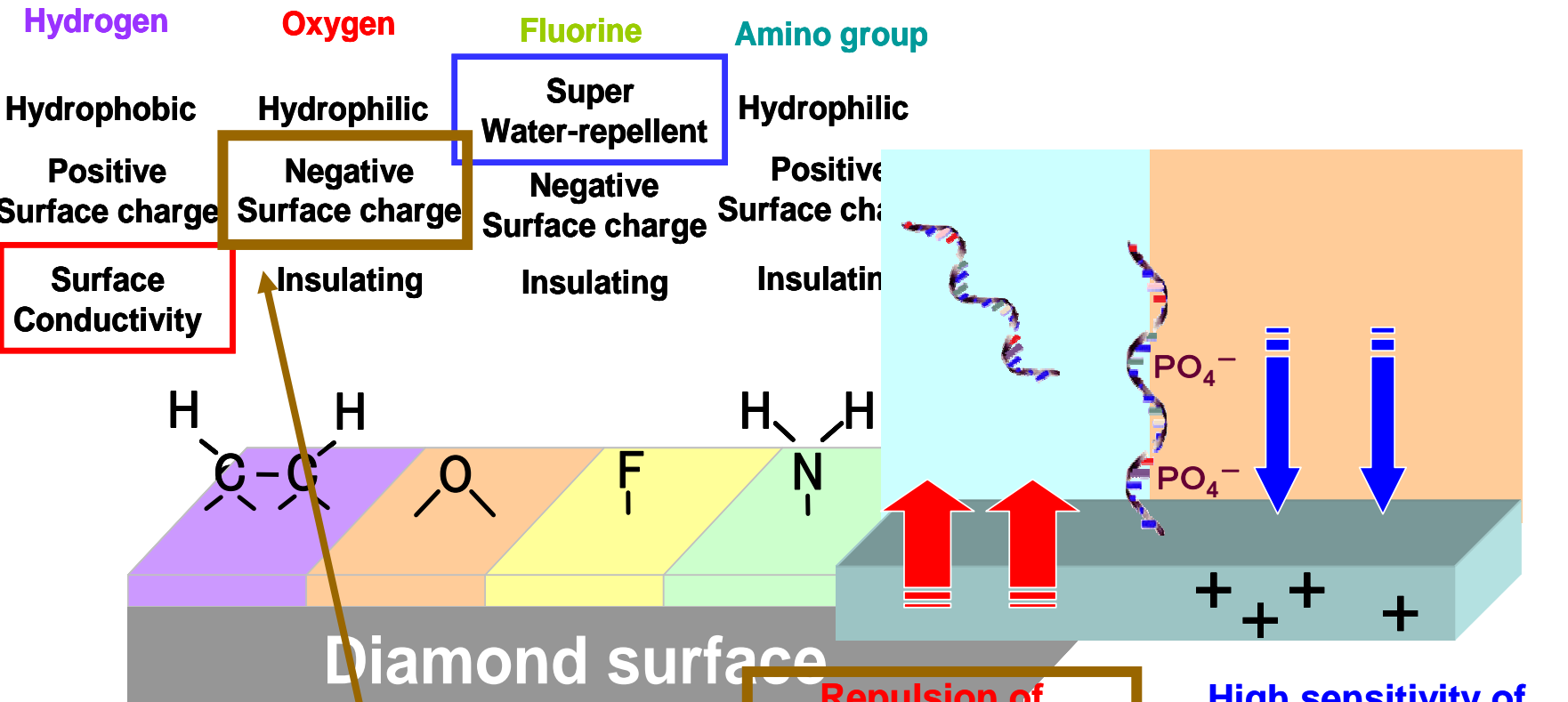
- Highly boron doped diamond exhibits superconductivity below 7K. Coexistence of **Super-** and Semiconductivity.
- The 2D hole gas (hole accumulation) can be caused by spontaneous polarization by H-termination. The surface stabilization might be carried out by passivation.
- Diamond MOSFET using aluminum oxide as gate insulator.
- Maximum cut-off frequency is  $\sim 50\text{GHz}$ . The estimated hole velocity is  $6 \times 10^6 \text{ cm/s}$ , which is a half of saturated hole velocity. The channel mobility at RF operation is evaluated to be  $100\text{-}200 \text{ cm}^2/\text{Vs}$ .
- The output power density exceeds that of Si LDMOS and GaAs MESFET.
- The best p-channel FET by diamond can coexist with the best n-channel FET by AlGaN/GaN.

# Outline for subject 3

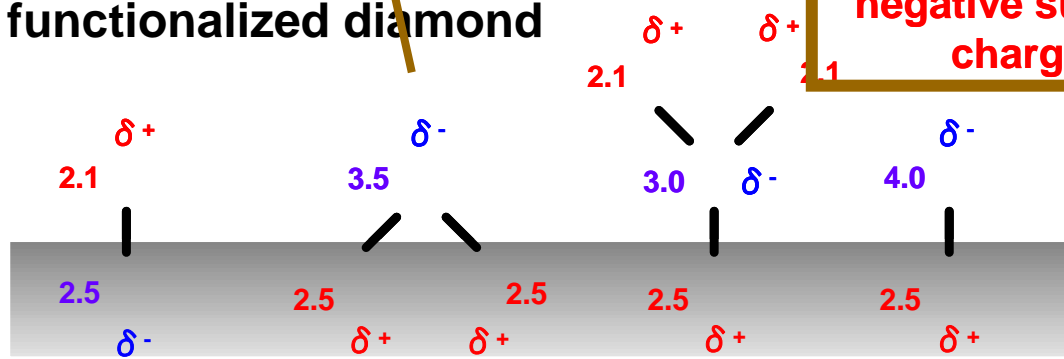
- **Why diamond for DNA sensor?**
- **DNA immobilization on modified diamond surfaces**
- **Diamond electrolyte solution-gate Field Effect Transistors (SGFETs)**
  - different from conventional Si ISFET*
- **DNA hybridization detection in static and real time**
- **Mismatch detection**
  - 1 base mismatch detection can be realible*

**Summary**

# DNA Repulsion by Surface Negative Charge



Electronegativity of partially functionalized diamond



Repulsion of nonspecific DNA by negative surface charge

High sensitivity of hybridized DNA by direct immobilization

# Direct immobilization of carboxylated probe DNA

## Used DNA sequence

**Target. DNA: Cy5 3'-GGTGCCTGATGAAGTTTTGAT-5'**

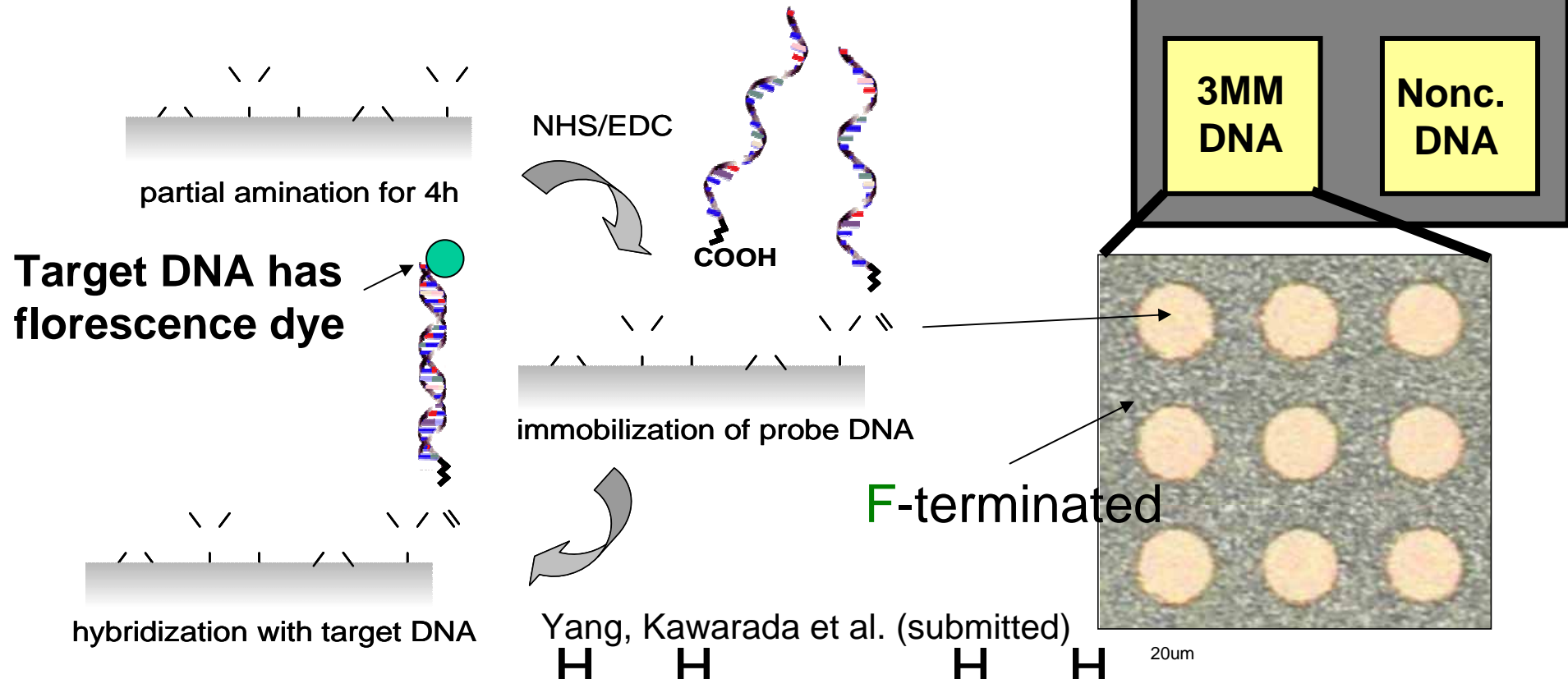
**Comp. probe DNA: HOOC-5'-CCACGGACTACTTCAAAACTA-3'**

**1MM probe DNA: HOOC-5'-CCACGGACTAGTTTCAAAACTA-3'**

**3MM probe DNA: HOOC-5'-CCA**G**GGACTAGTTCA**A**ACTA-3'**

**Noncomp. probe DNA: HOOC-5'-**ATCGATCGATCGATCGATCGA**-3'**

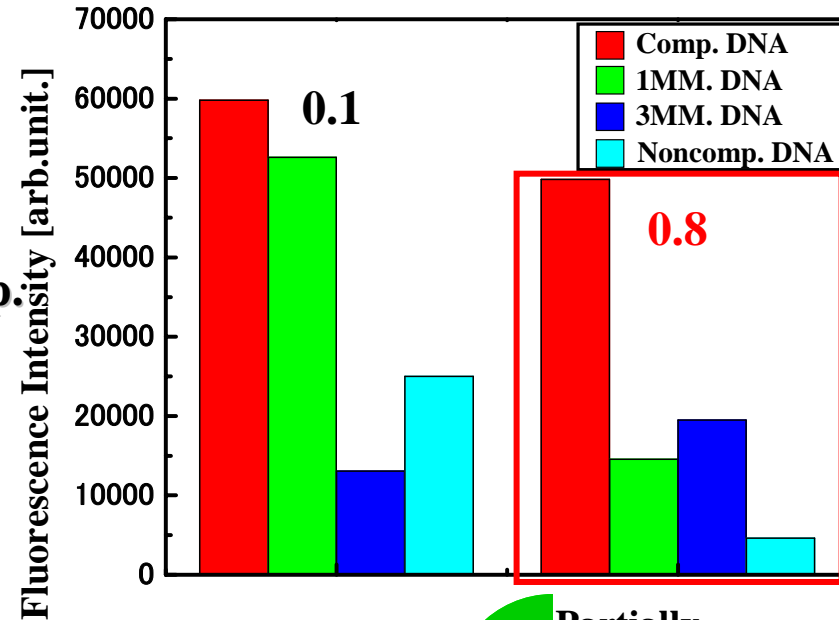
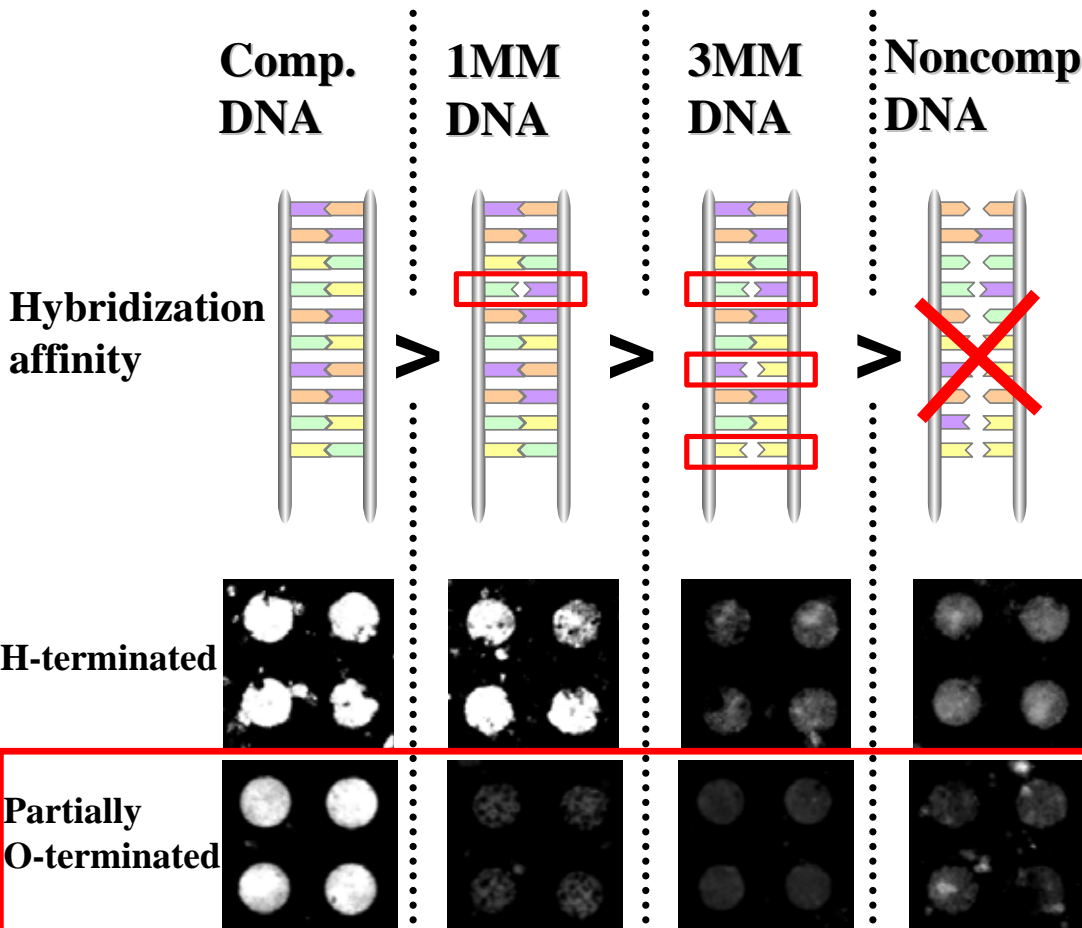
## Direct immobilization of DNA on functionalized diamond



# 1 base mismatch detection by fluorescence

Target DNA conc. 100nM  
in  $2 \times$  SSC buffer solution  
Hybridization temp.  $55^\circ\text{C}$   
Hybridization time 30min

Yang, Kawarada et al. (submitted)



H-terminated

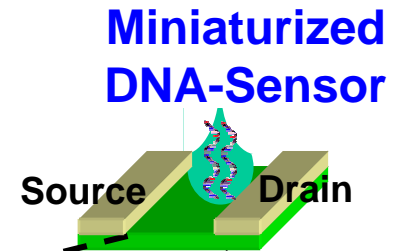
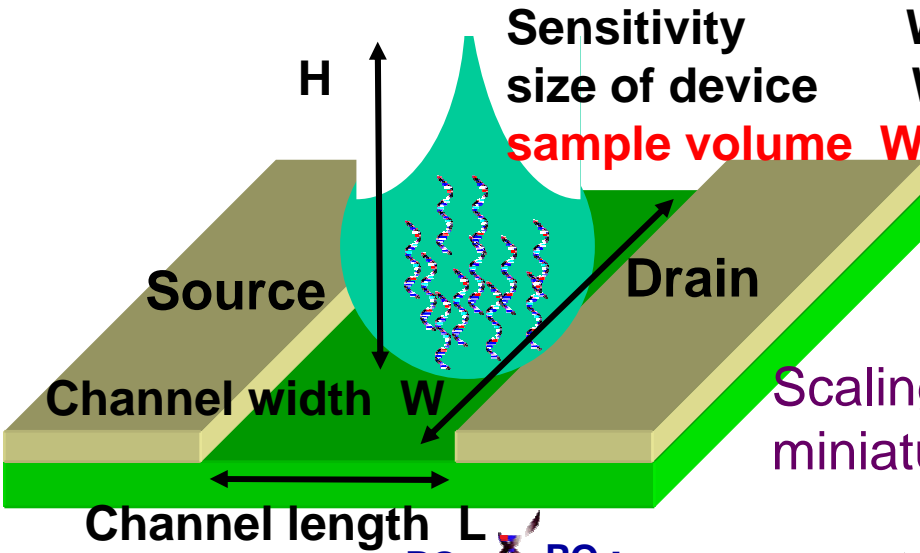
Partially O-terminated

High Contrast

Used for FET  
Detection

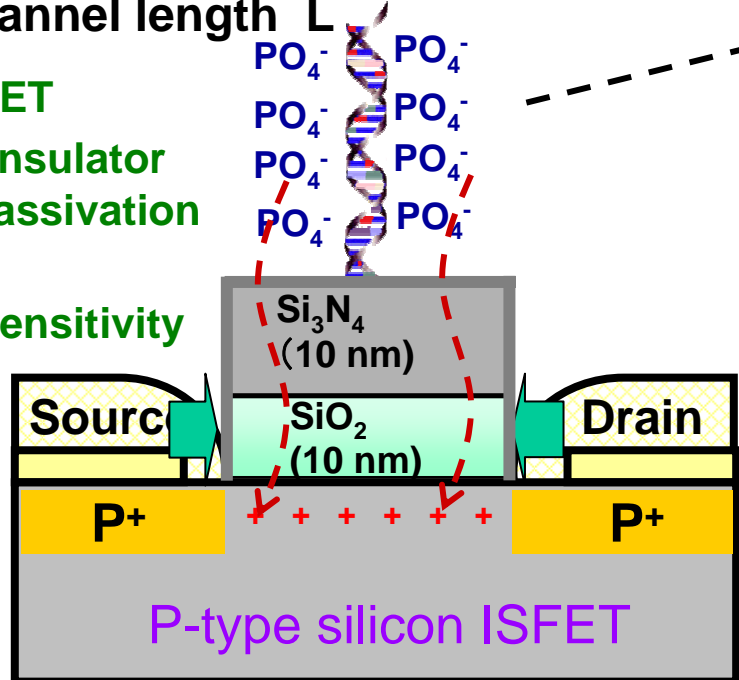
# Advantages of diamond electrolyte solution gate FET (SGFET) compared with Si ion sensitive FET

Sensitivity	W/L	1	sensitivity	1
size of device	WL	1	chip size	$1/k^2$
sample volume	WLH	1	sample volume	$1/k^2 \sim 1/k^3$

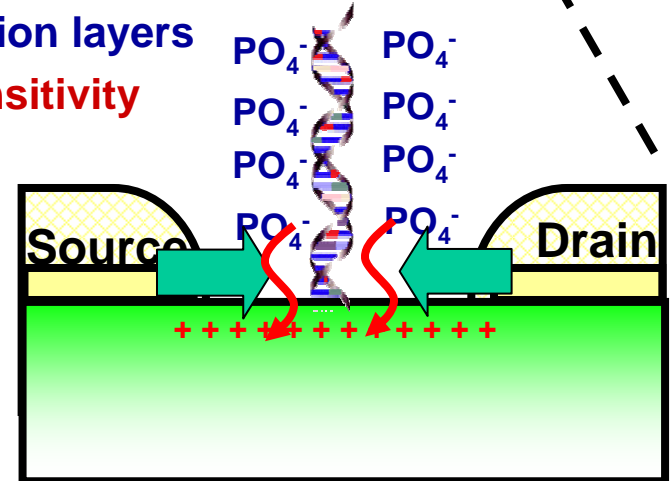


Scaling rule of transistor miniaturization:  $1/k$

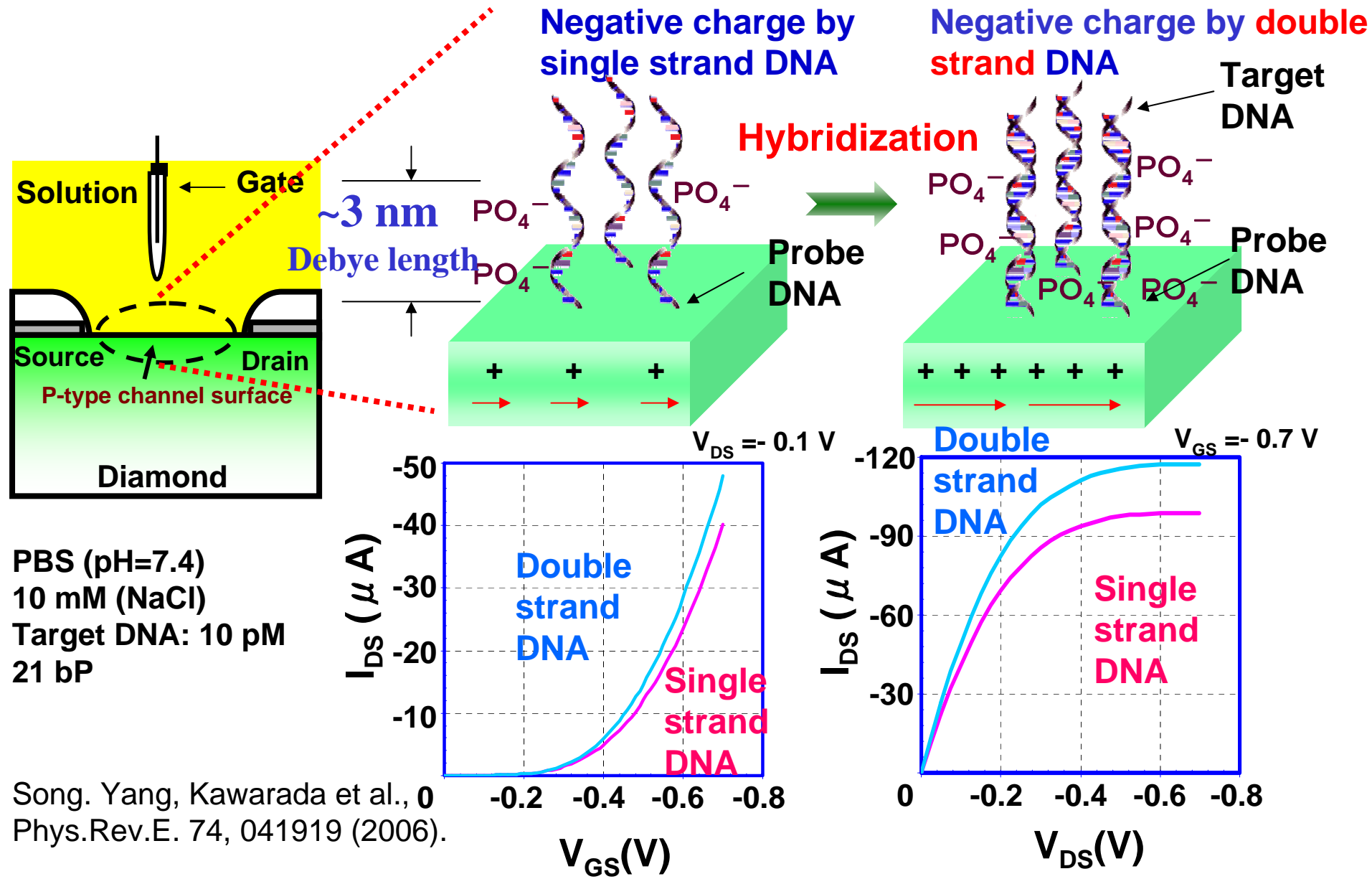
Si-ISFET  
Gate insulator and passivation layers  
Low sensitivity



Diamond SGFET  
Directly exposed channel surface to solution without passivation layers  
High sensitivity



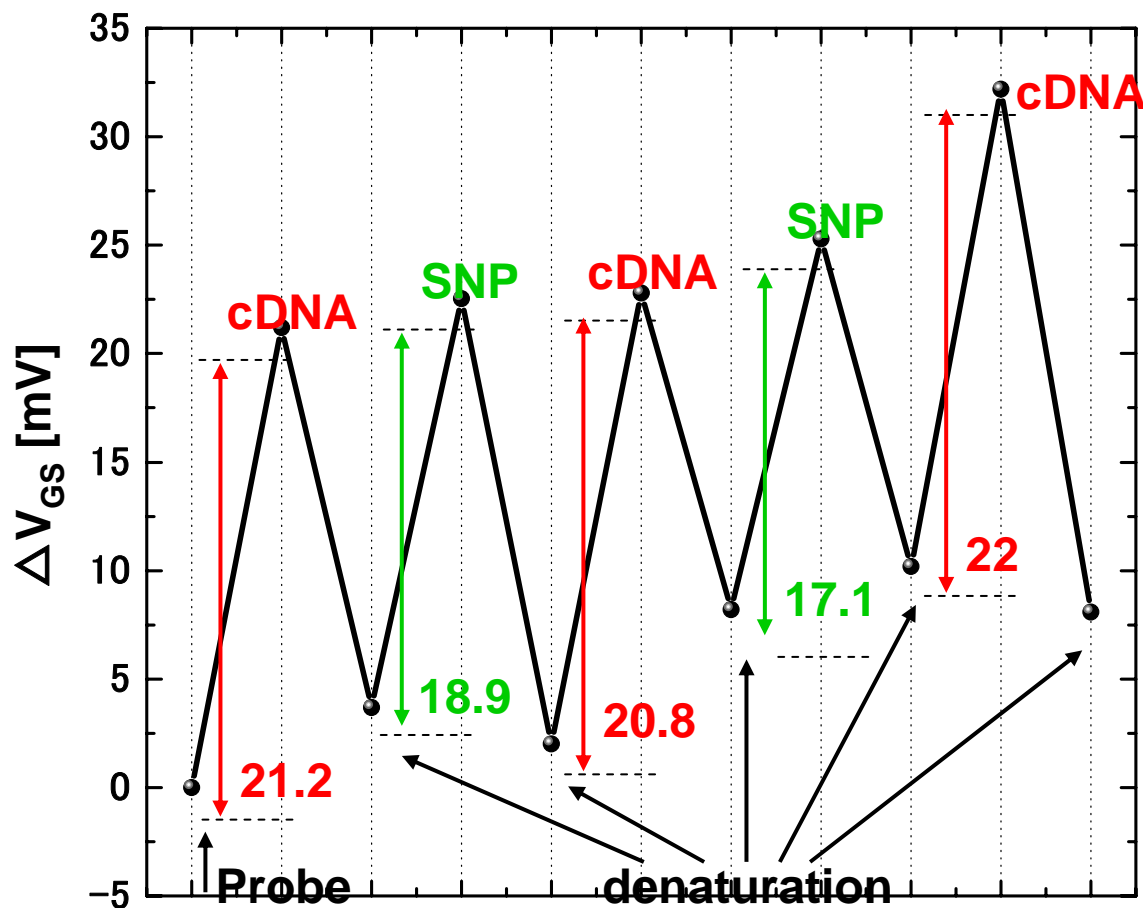
# Holes are induced by intrinsic negative charge of DNA after hybridization



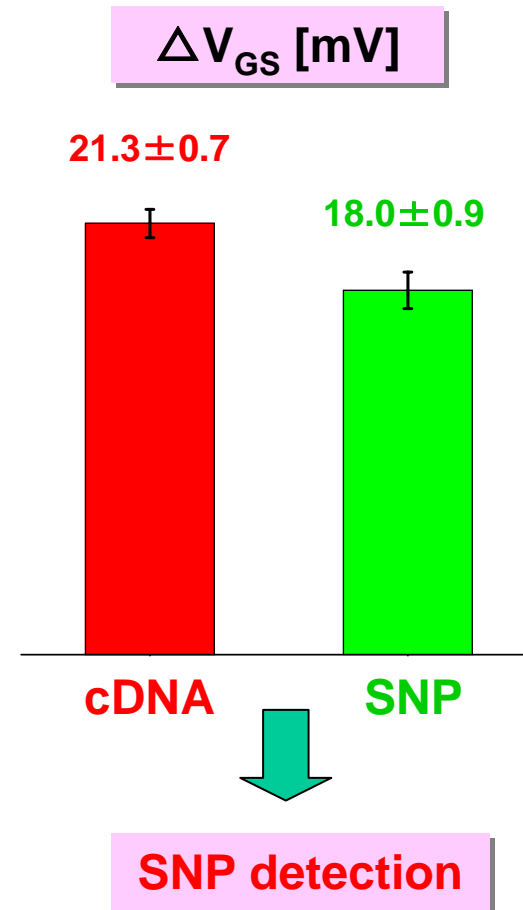


# SNPs (1 bp mismatch) detection in static measurement

Repeat hybridization (cDNA or SNP) and denaturation



Song, Yang, Kawarada et al.,  
Phys.Rev.E. 74, 041919 (2006).



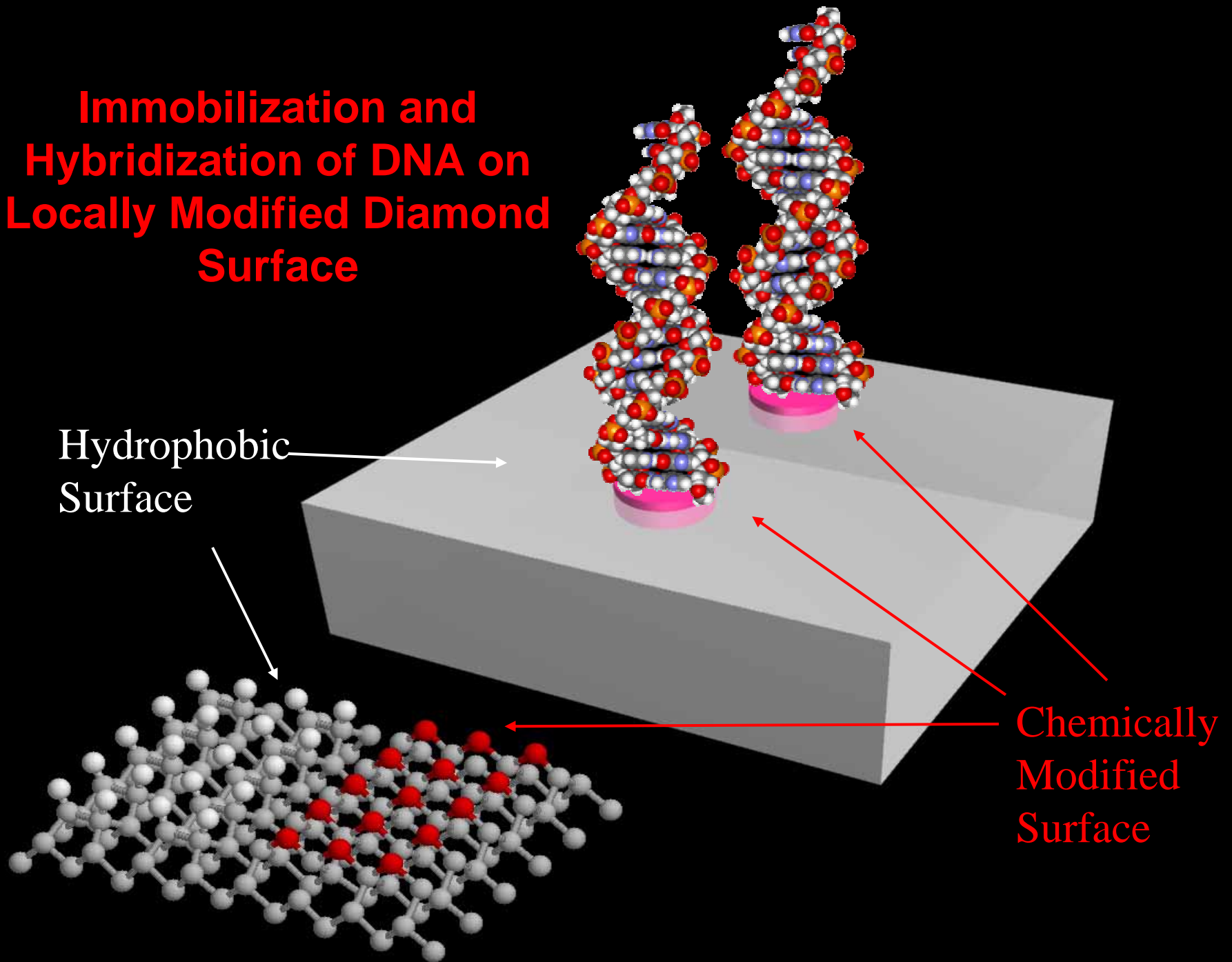
Hybridization temperature : RT  
Buffer sol. : 1mM PBS DNA conc. : 100 [pM]  
 $I_{DS} = -10$  [ $\mu$ A]  $V_{DS} = -0.1$  [V]

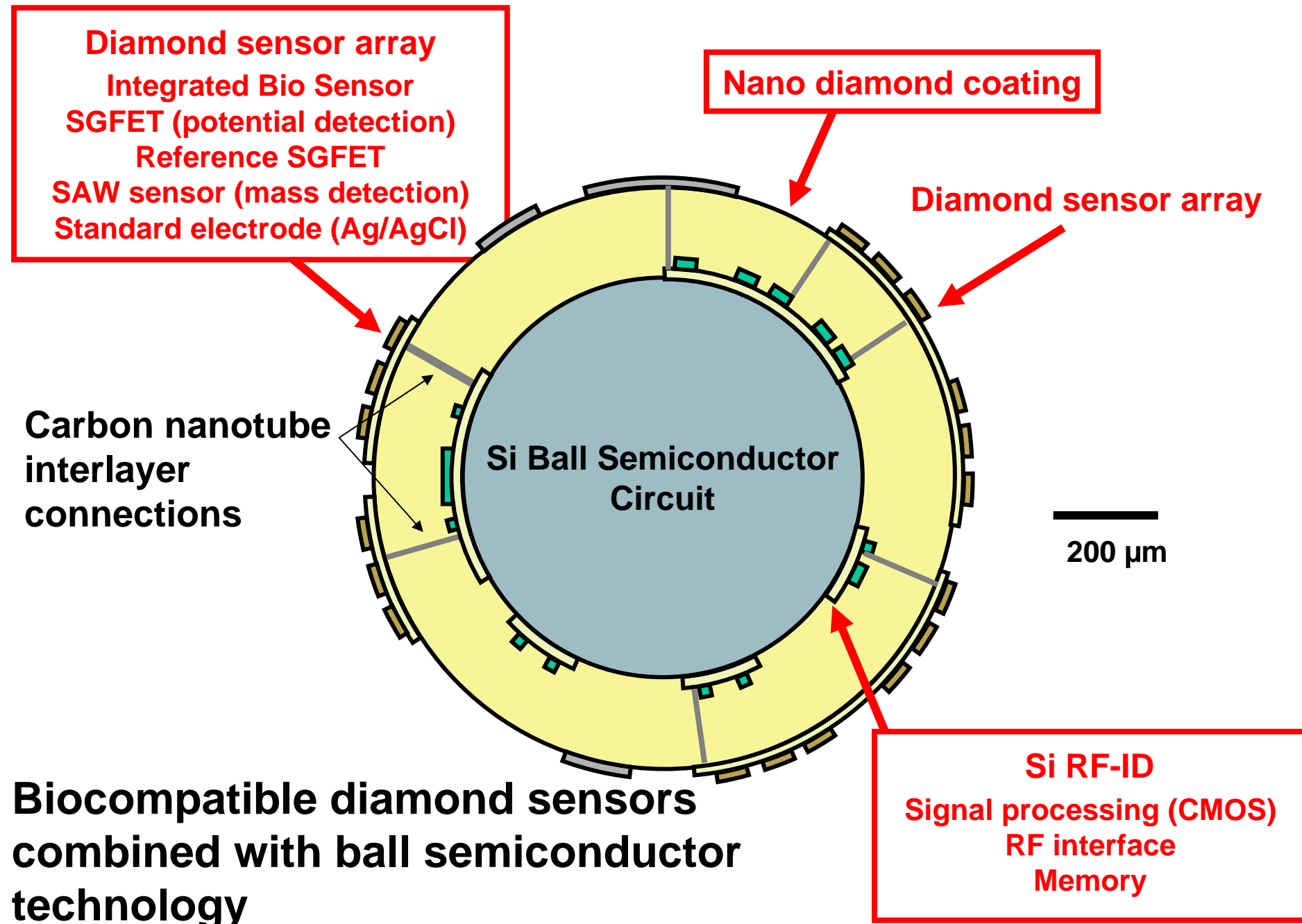
# Summary II

Using directly immobilized probe DNAs on functionalized diamond, the detection of target DNAs has been investigated using two methods: fluorescence microscopy and SGFETs.

- Discrimination of single base (1bp) mismatched DNA by fluorescence microscopy by controlling surface termination.
- We realized DNA biosensor using solution gate (SG)FET to detect molecular charge.
- Large gate voltage shift of 20-30mV repeatable in the cycles of hybridization & denaturation indicating reusability.
- Distinction between cDNA, 1bp-mismatch and ncDNA sequences.
- **In the future application**, charge detection of **RNA interaction with immobilized proteins**

# Immobilization and Hybridization of DNA on Locally Modified Diamond Surface





# Semiconductor is a stone where we sit

- “Sit on a stone for **3 years**”

石の上にも3年

This old Japanese saying means that  
“with patience something can be realized”

- “Need to stay on **one** semiconductor for **30 years**”

**1種**の半導体に**30年**必要

What we need here is not only patience,  
but “**staying power**” and “**sustainable development**”.